

FIG. 1

FIG. 2B

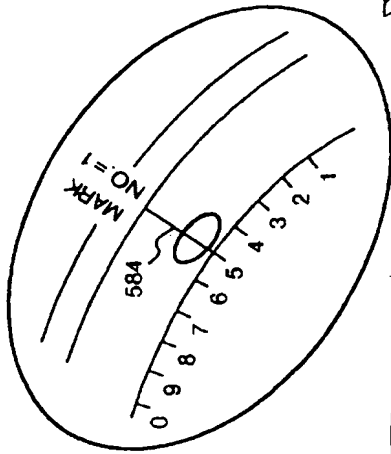


FIG. 2C

NONREFLECTIVE PITS ARE  
FORMED IN RADIAL DIRECTION

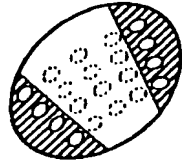


FIG. 2A

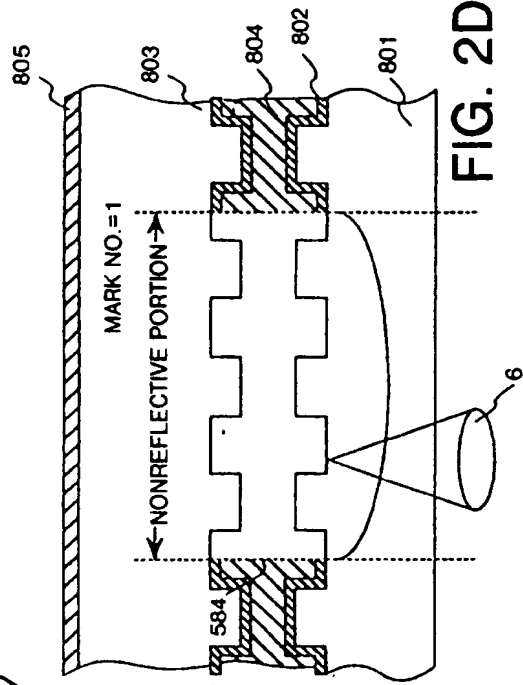
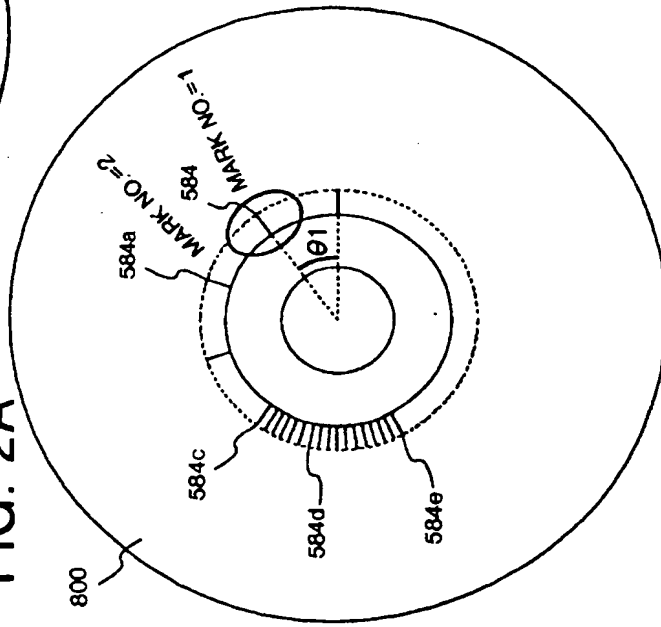


FIG. 2D

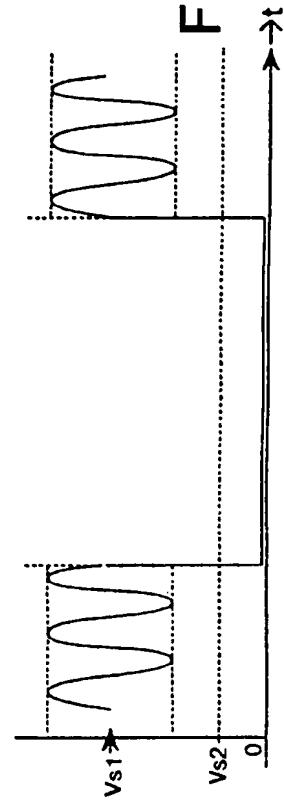


FIG. 2E

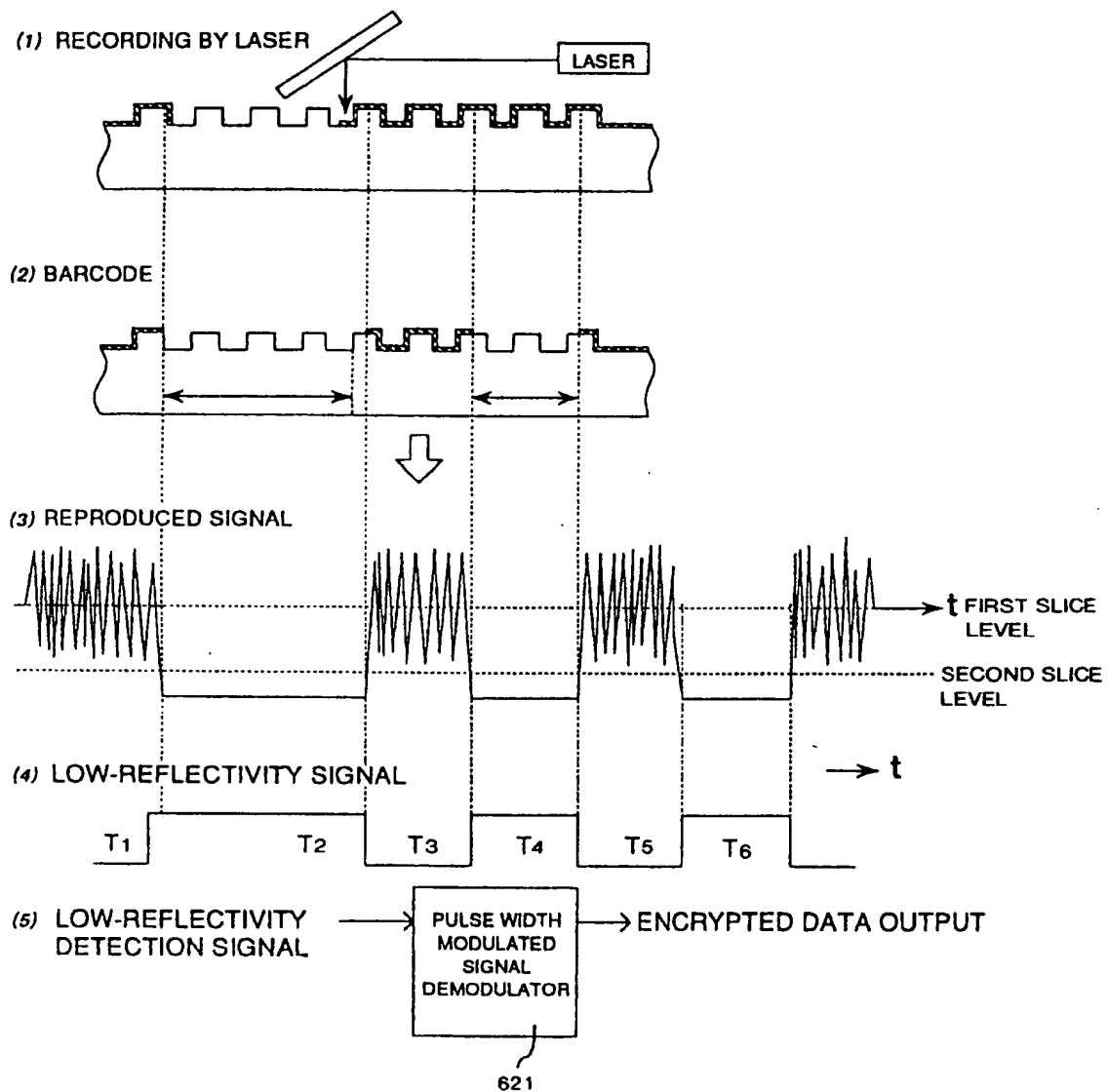
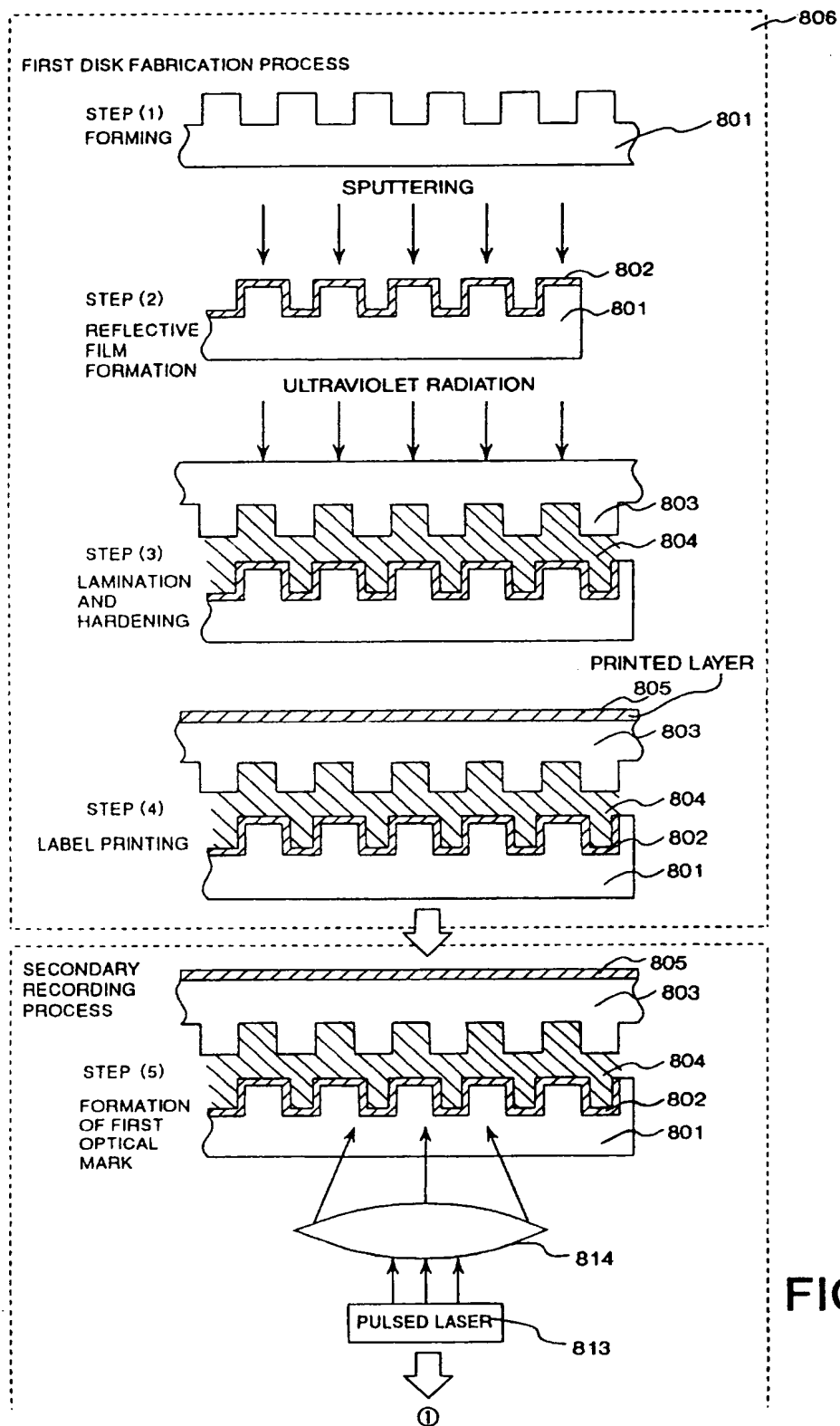
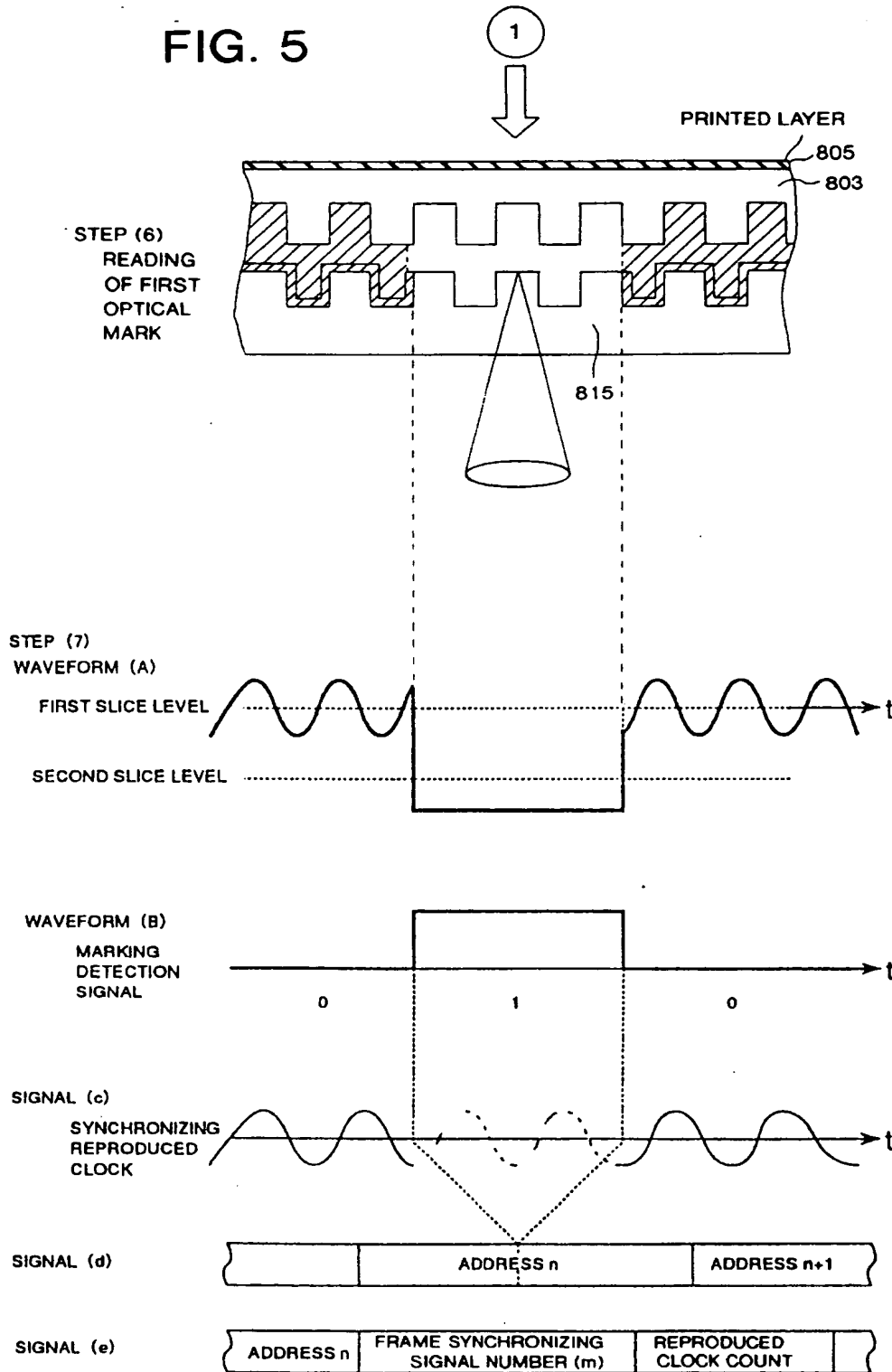


FIG. 3



**FIG. 4**

FIG. 5



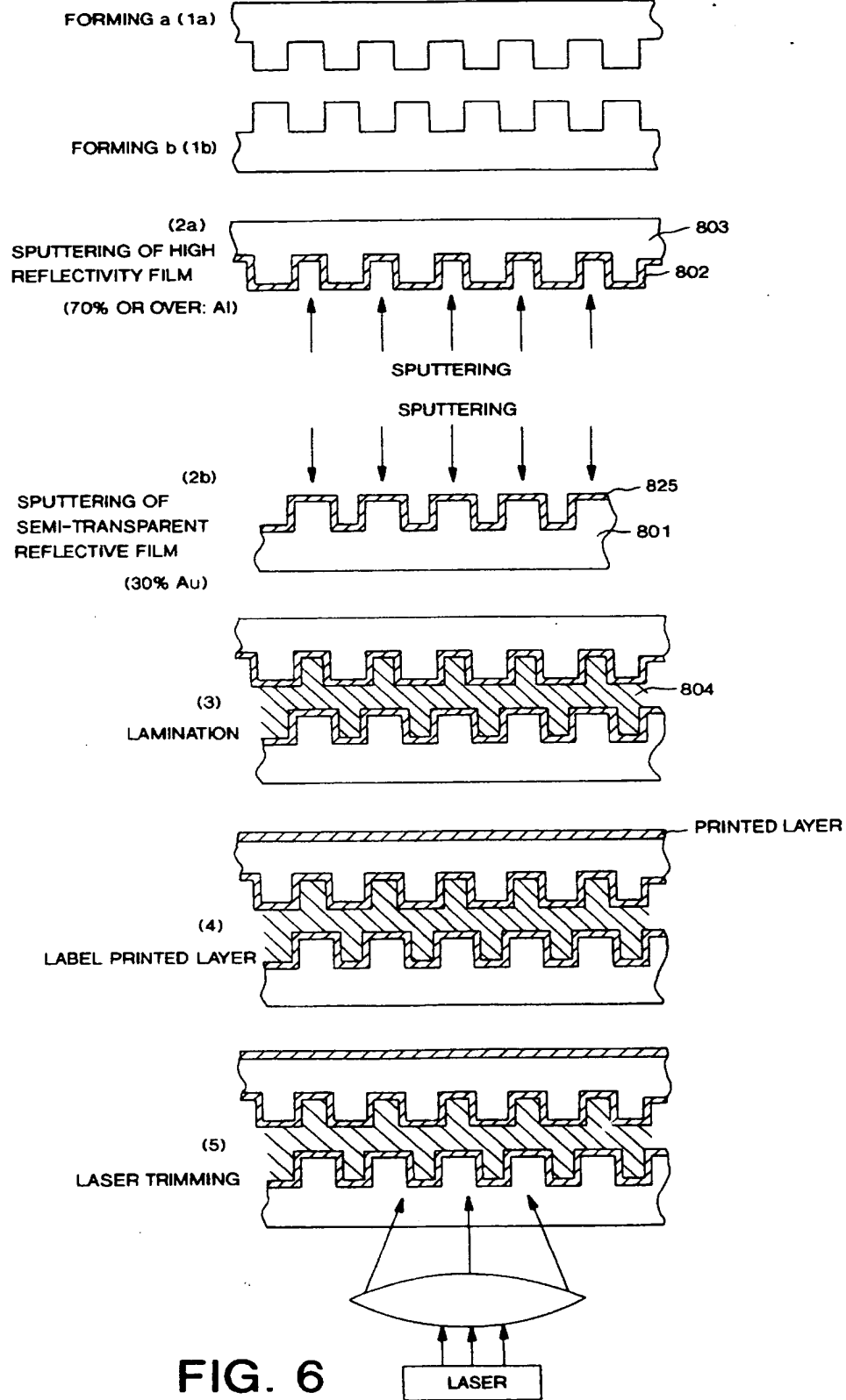


FIG. 6

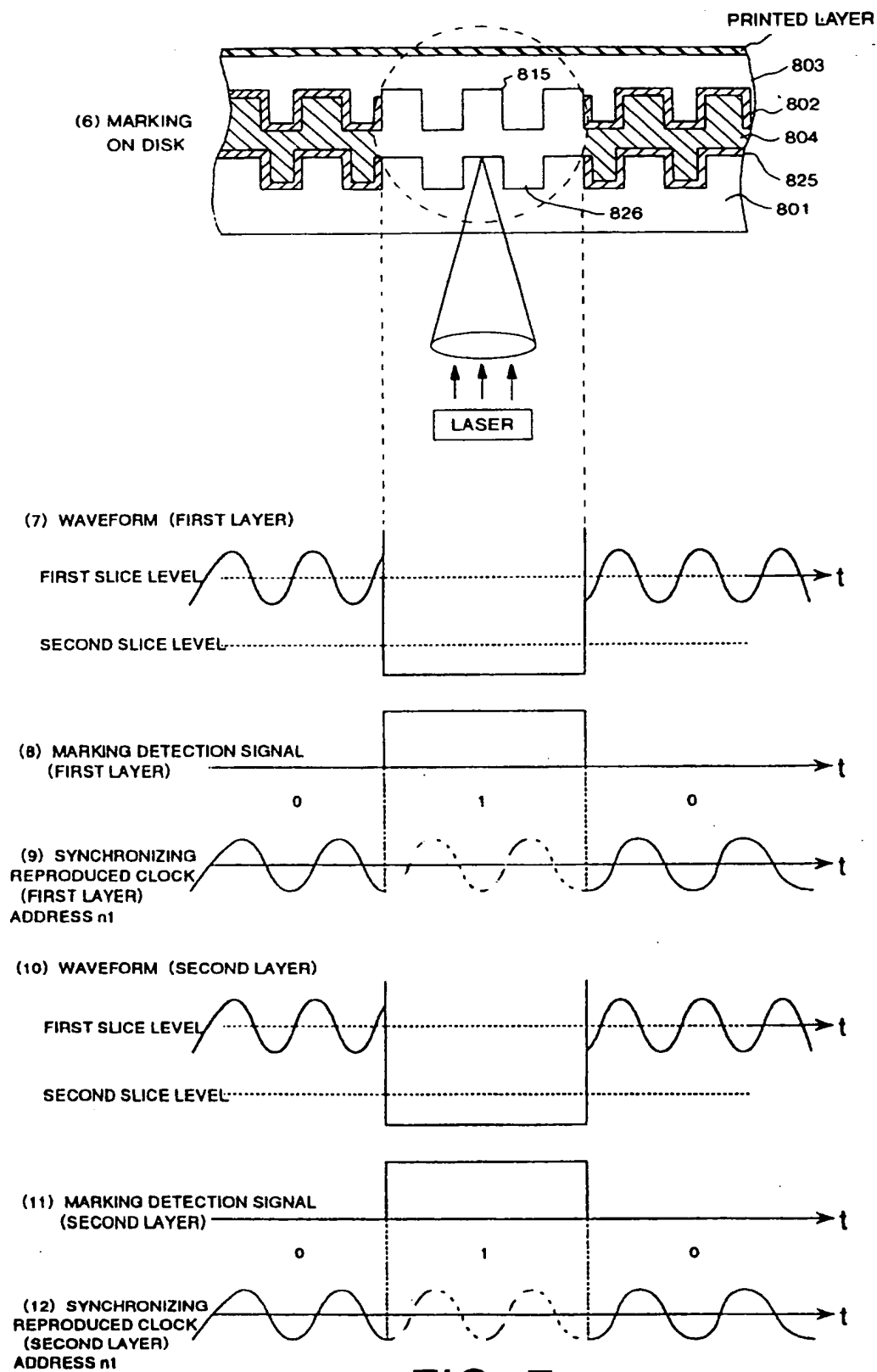


FIG. 7

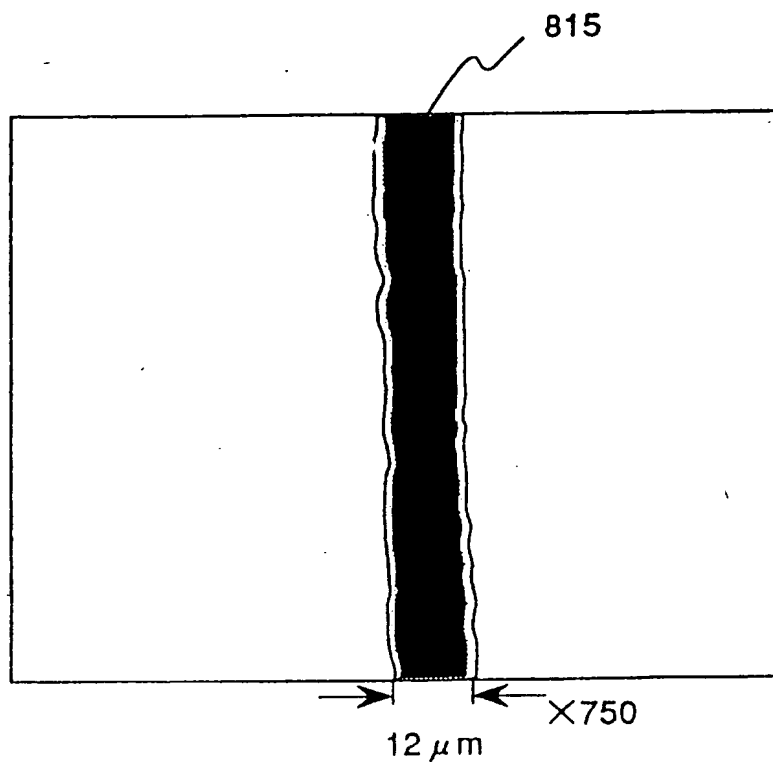


FIG. 8A

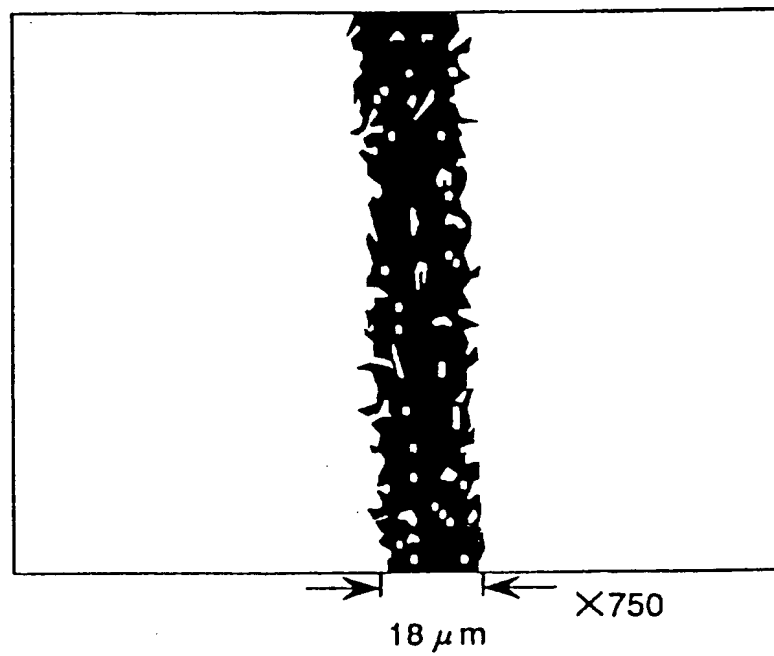
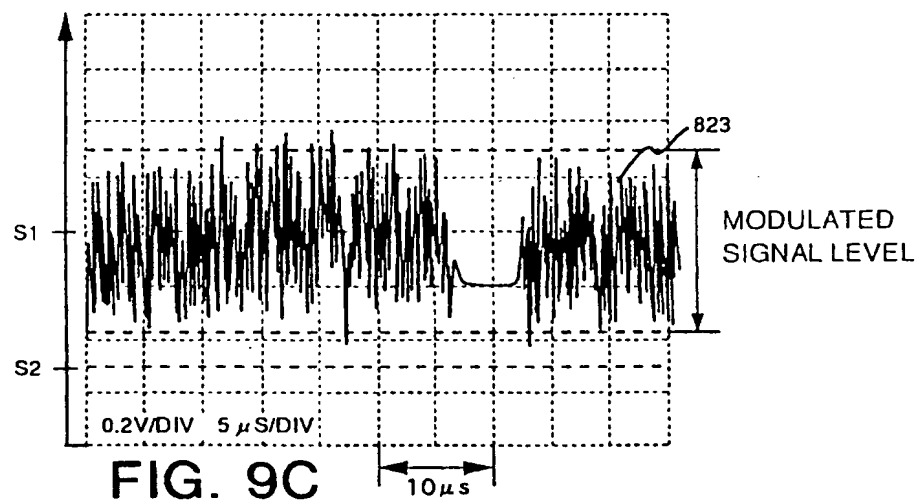
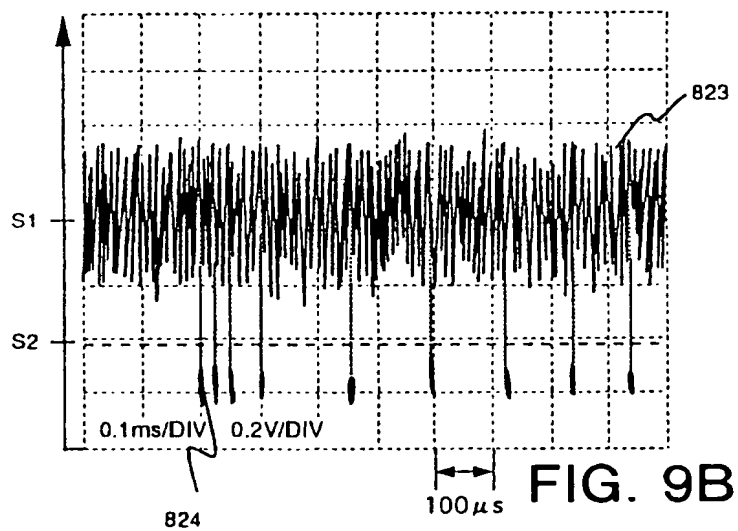
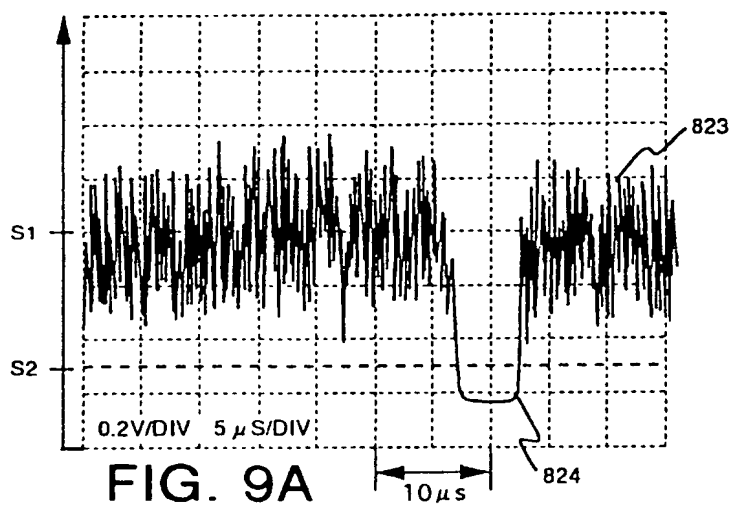


FIG. 8B





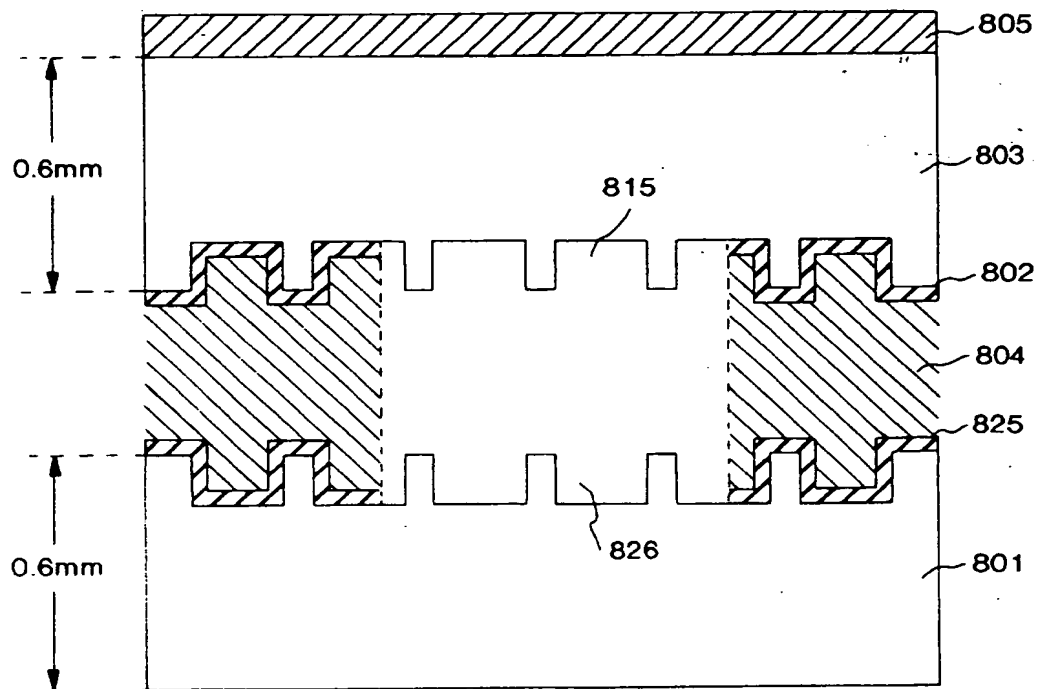


FIG. 10A

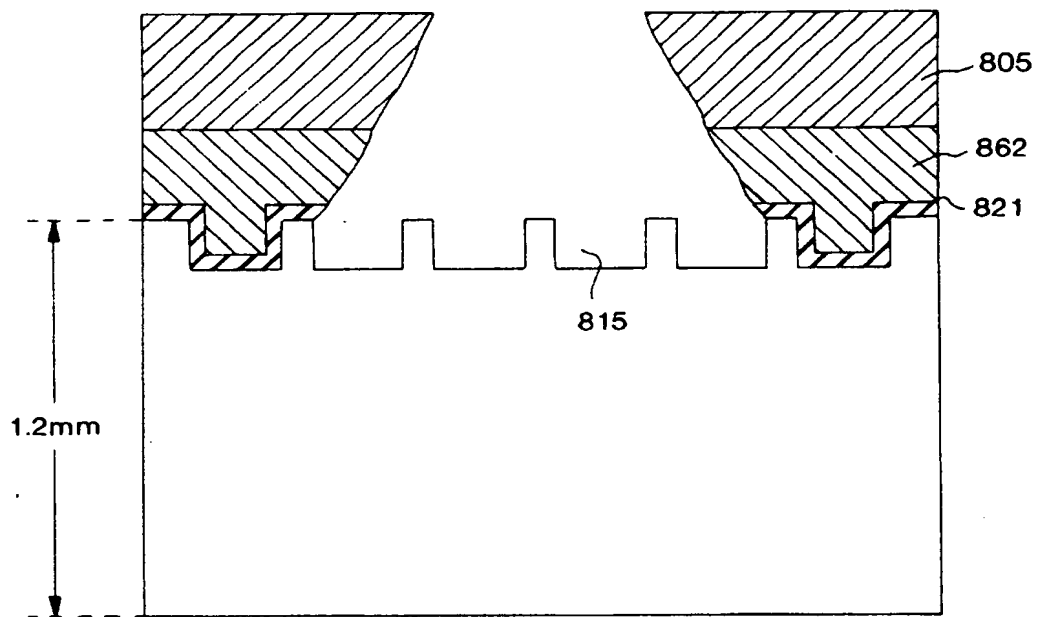


FIG. 10B

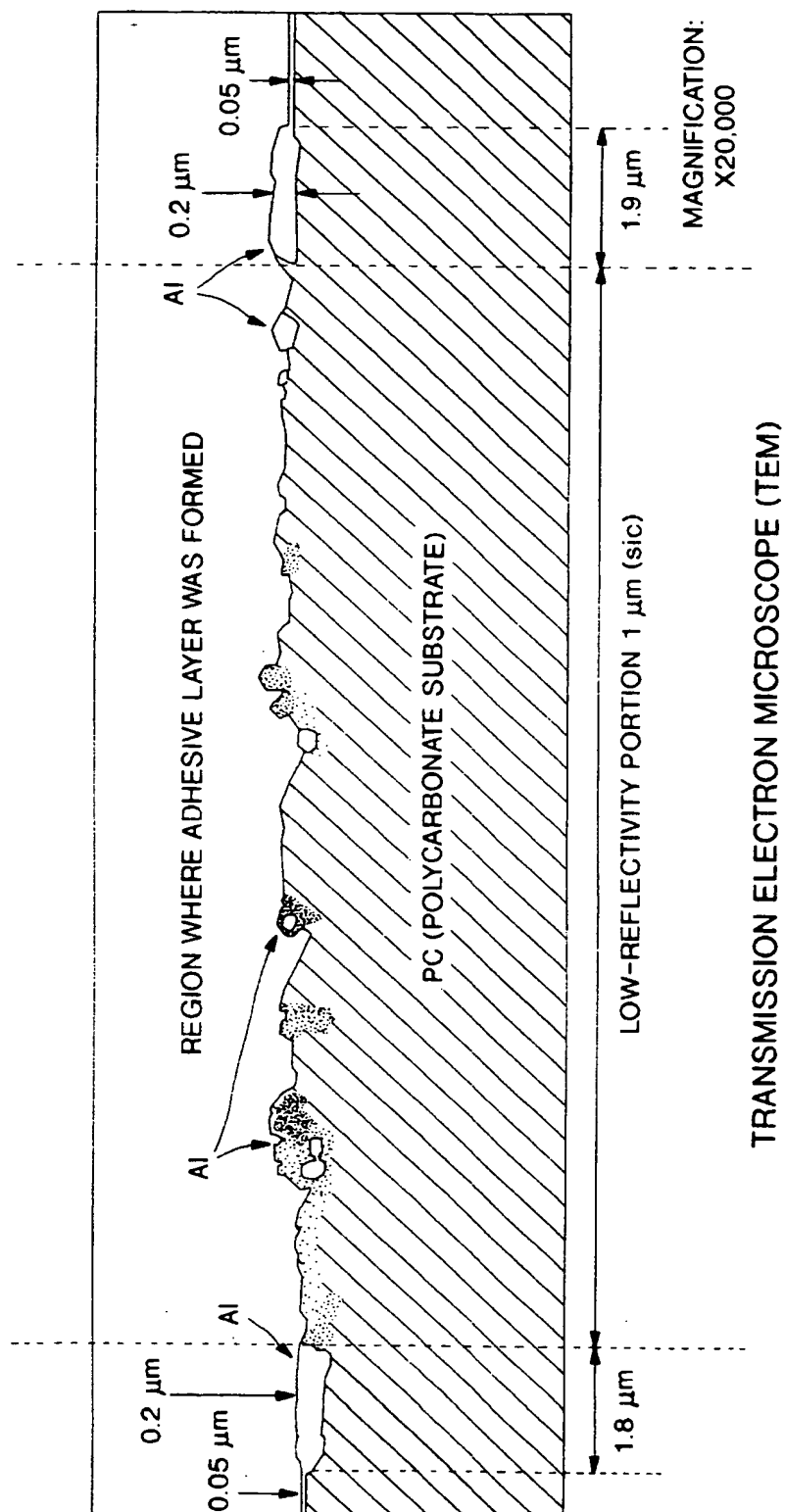


FIG. 11

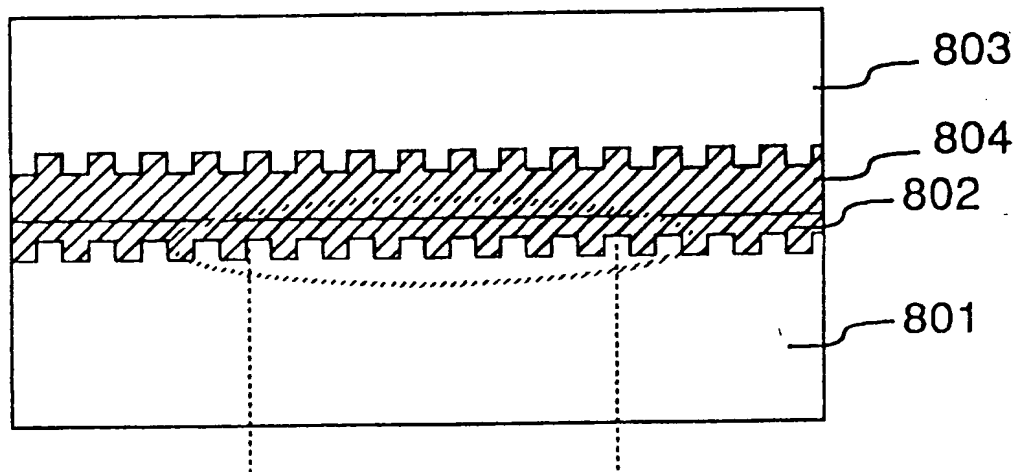


FIG. 12A

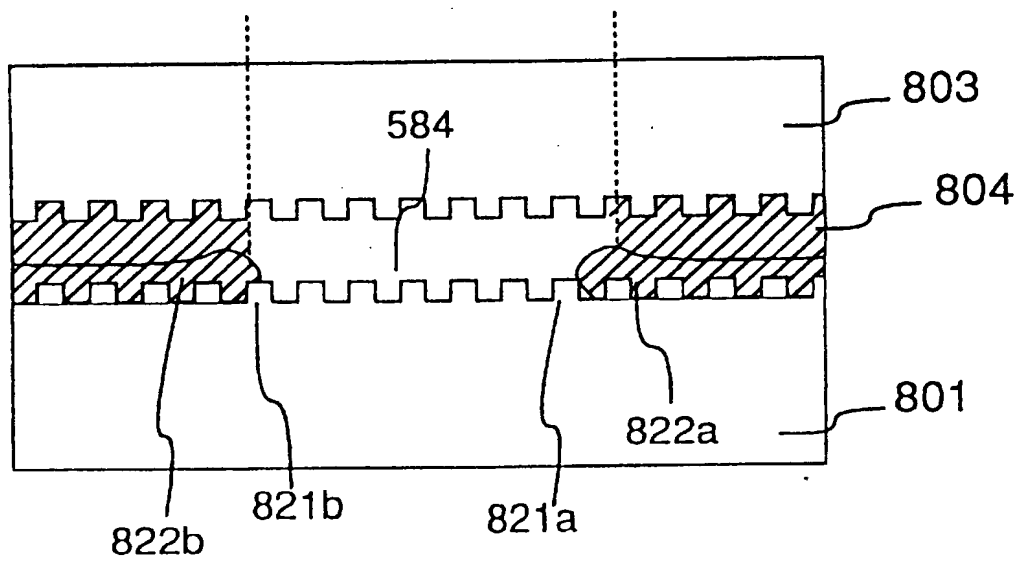
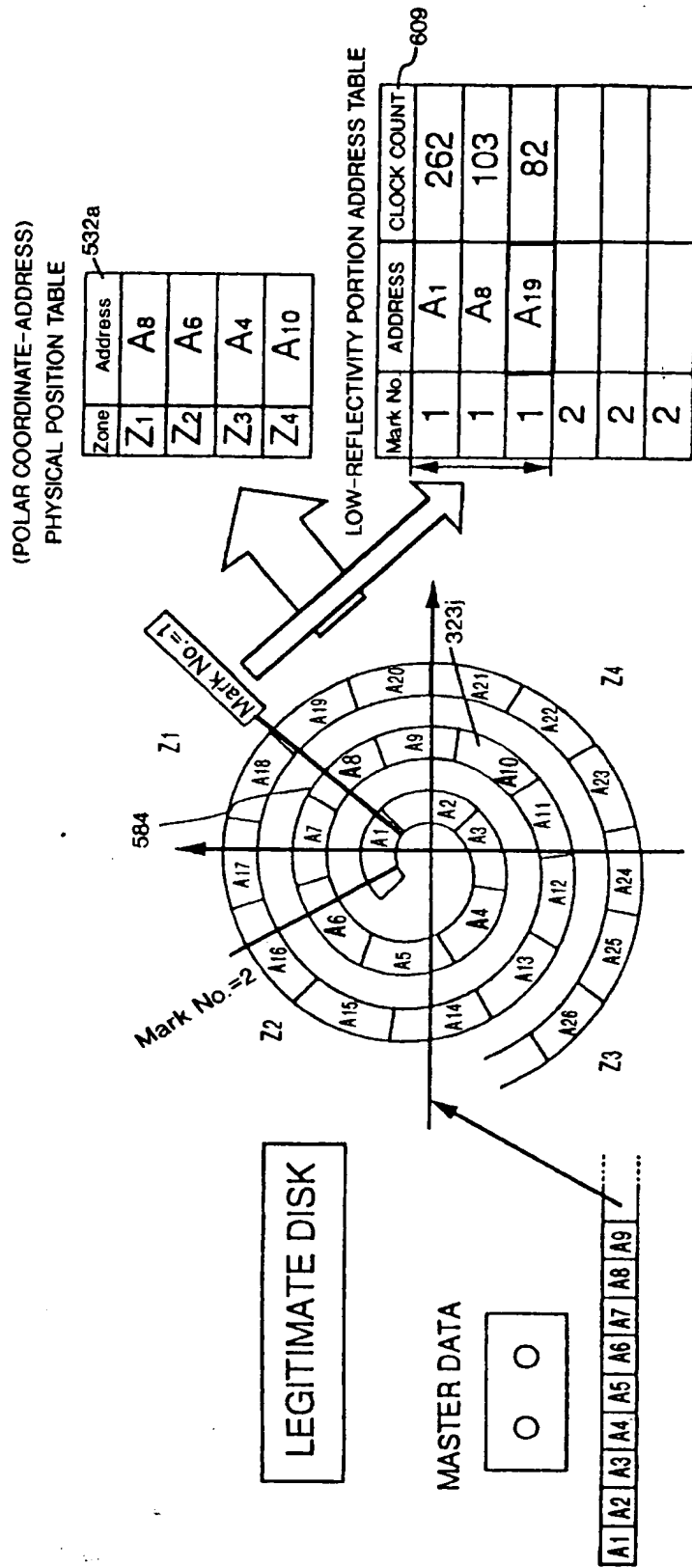
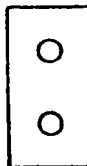


FIG. 12B



MASTER DATA



A1 A2 A3 A4 A5 A6 A7 A8 A9

ILLEGALLY DUPLICATED CD

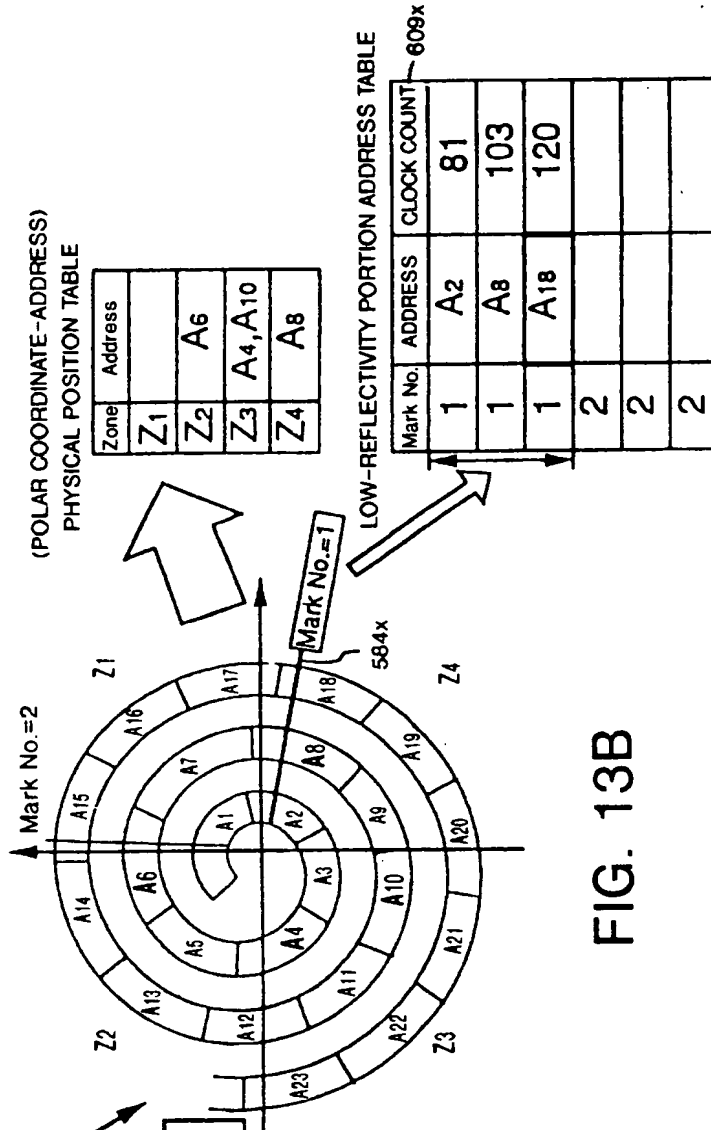


FIG. 13B

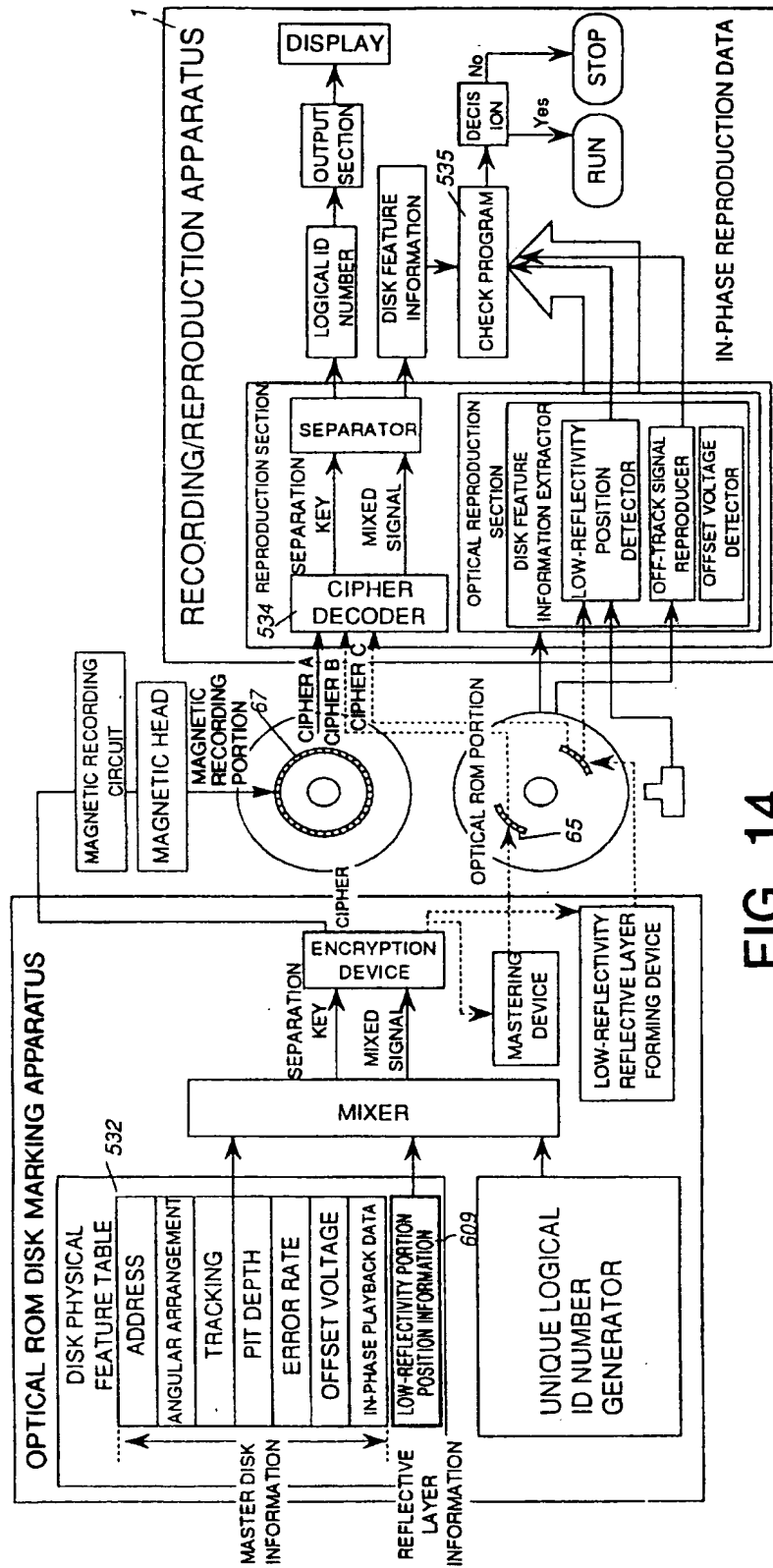
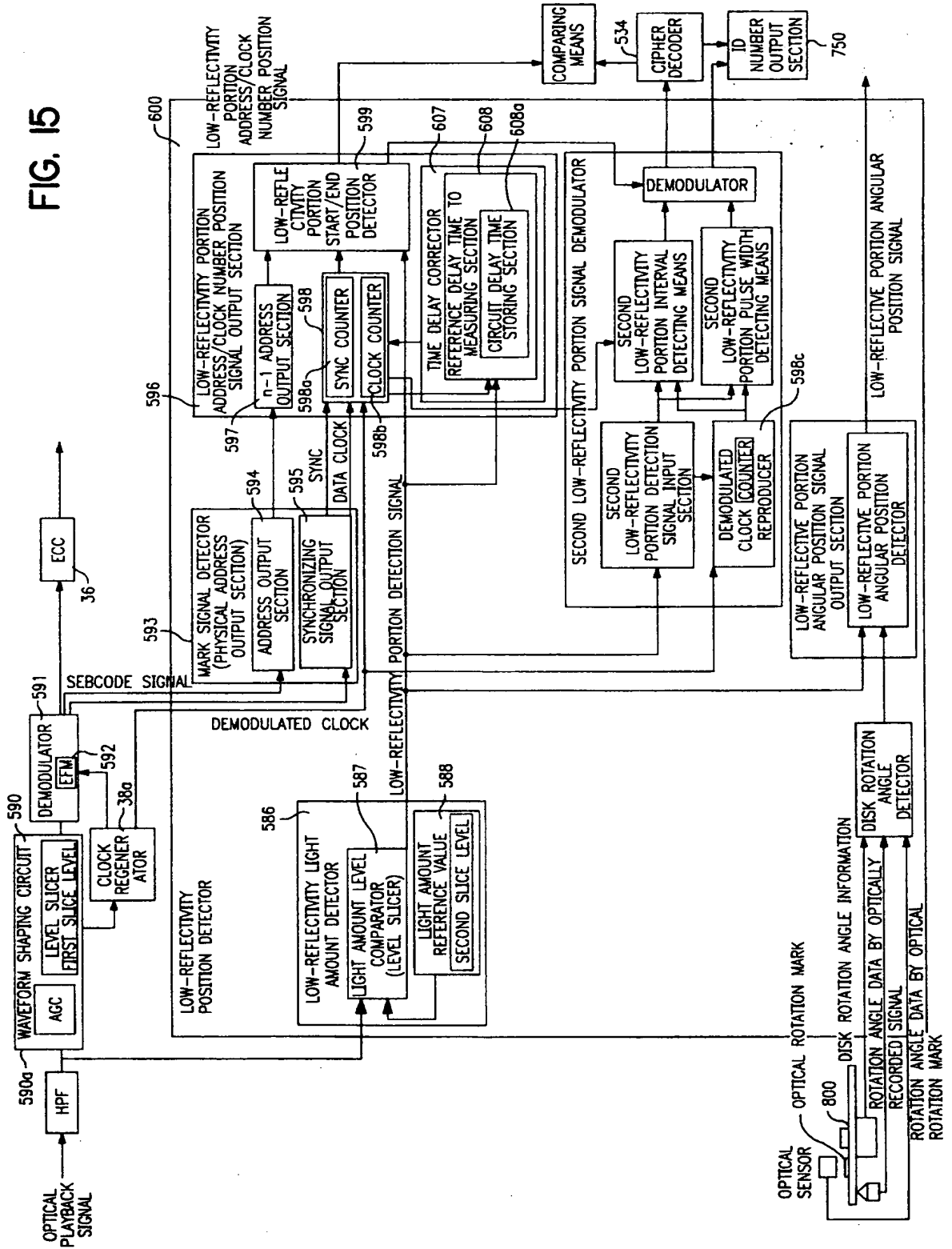


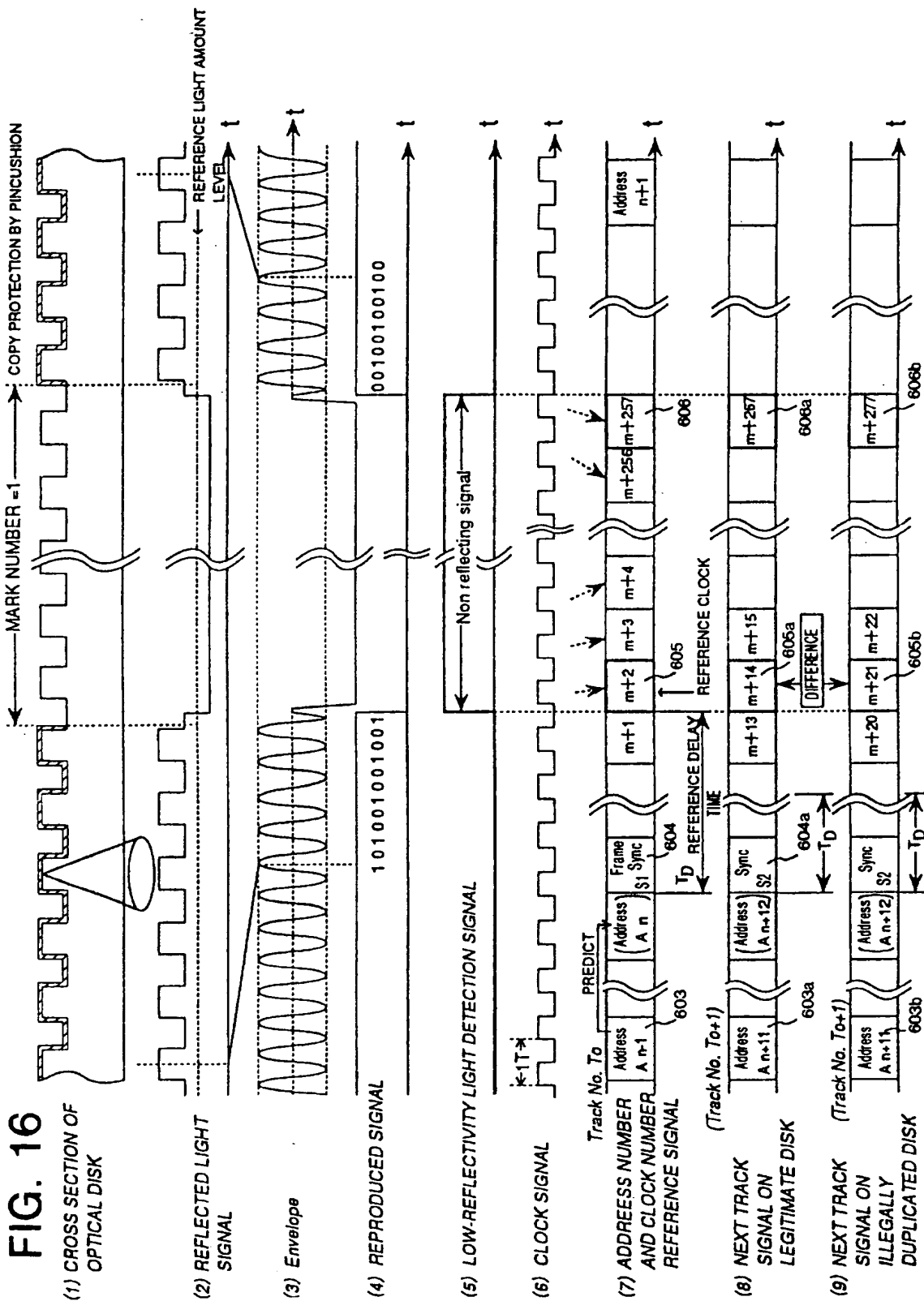
FIG. 14

FIG. 15





# FIG. 16



LEGITIMATE DISK

LOW-REFLECTIVITY PORTION ADDRESS TABLE

609

MARK NO.	START POSITION			END POSITION		
	ADDRESS	Sync No	CLOCK NUMBER	ADDRESS	Sync No	CLOCK NUMBER
1	A n	S <sub>1</sub>	m+2	n		m+257
1	A n+12	S <sub>2</sub>	m+14	n+12		m+267
1	A n+23		m+25	n+23		m+300
:	:		:	:		:
2	A n+1		m+15	n+1		m+160
2	A n+13		m+85	n+13		m+250
2	A n+24		m+68	n+24		m+210
10	A n+9					
10						

PLANNING

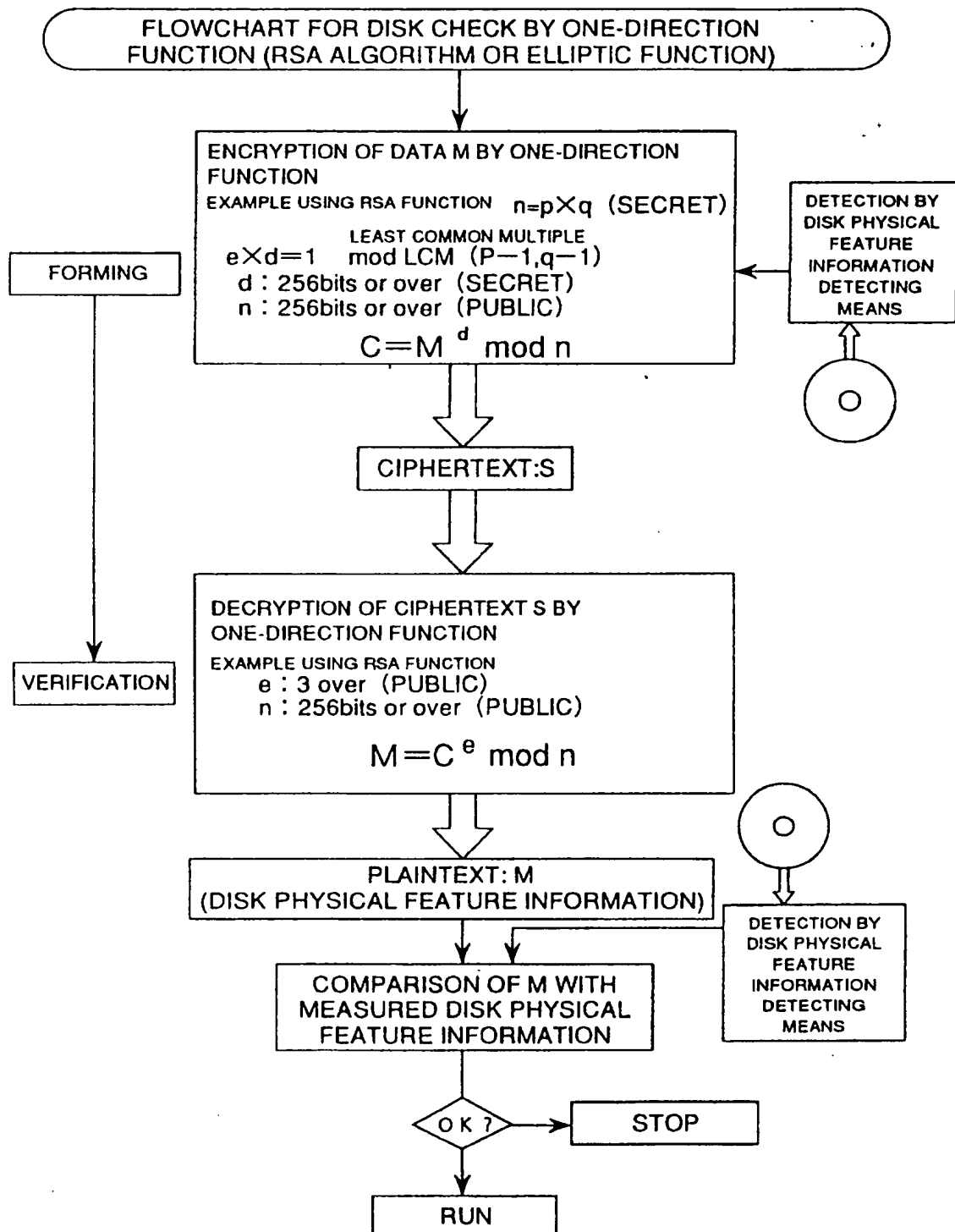
ILLEGALLY DUPLICATED DISK

LOW-REFLECTIVITY PORTION ADDRESS TABLE

609x

MARK NO.	START POSITION			END POSITION		
	ADDRESS	Sync No	CLOCK NUMBER	ADDRESS	Sync No	CLOCK NUMBER
1	n	S <sub>1</sub>	m+2	n		m+257
1	n+12	S <sub>2</sub>	m+21	n+12		m+277
1	n+22		m+4	n+22		m+230
:	:		:	:		:
2	n+1		m+36	n+1		m+190
2	n+13		m+120	n+13		m+281
2	n+25			n+25		
10	n+9					
10						

FIG. 17



**FIG. 18**

# DIAGRAM SHOWING DIFFERENT PHYSICAL LOCATIONS OF LOGICAL ADDRESSES ON DIFFERENT MASTER DISKS

PHYSICAL LOCATIONS OF THE SAME LOGICAL ADDRESSES ON MASTER DISKS PRODUCED ON DIFFERENT DAYS

- FIRST MASTER DISK ○  $CLV=1.231m/sec(SRC\#\#2 MISI)$   
 SECOND MASTER DISK ●  $CLV=1.245m/sec(FZ-SJ1951A 3)$   
 THIRD MASTER DISK ▲  $CLV=1.308m/sec(FZ-SJ1951AT 8)$

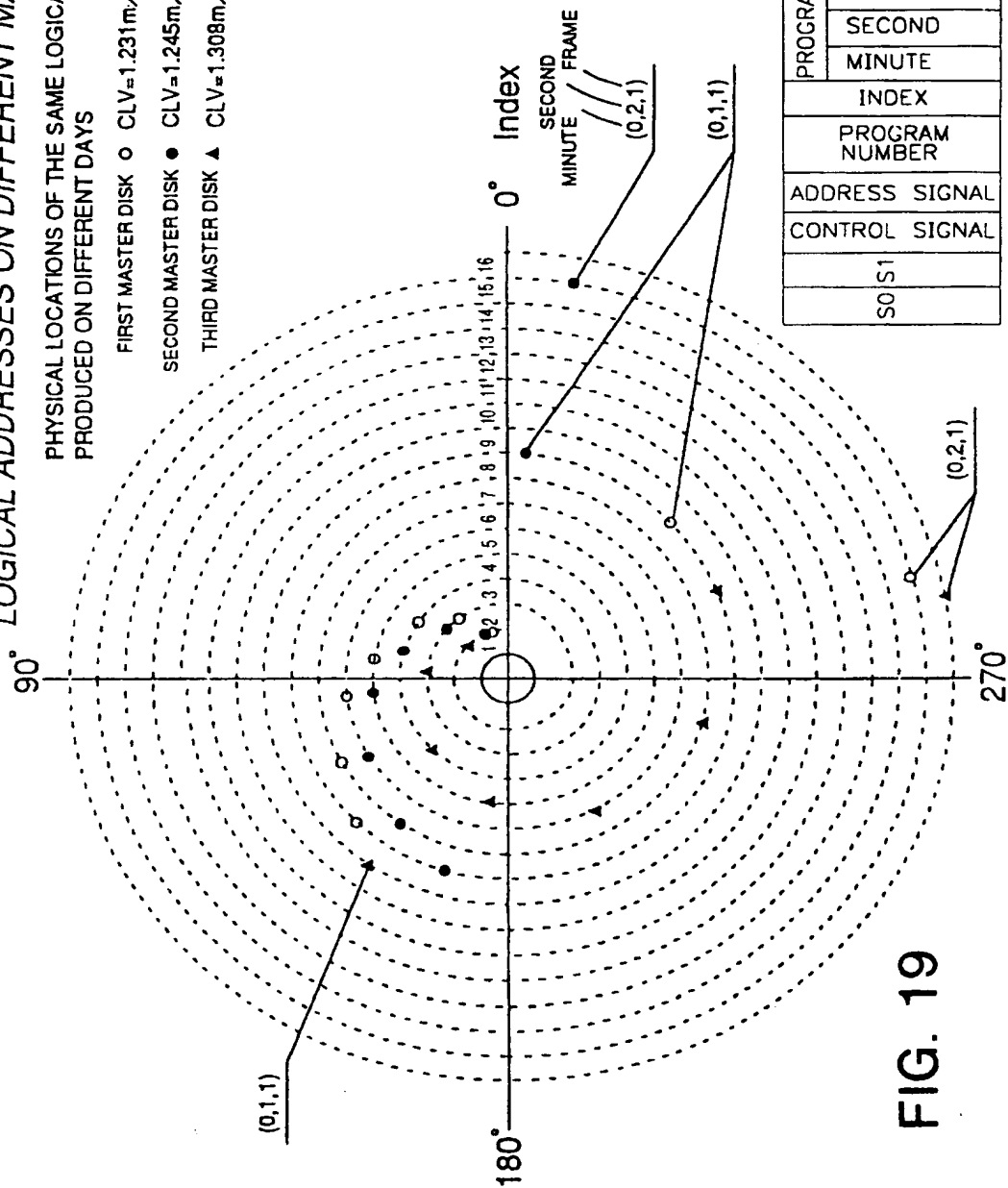


FIG. 19

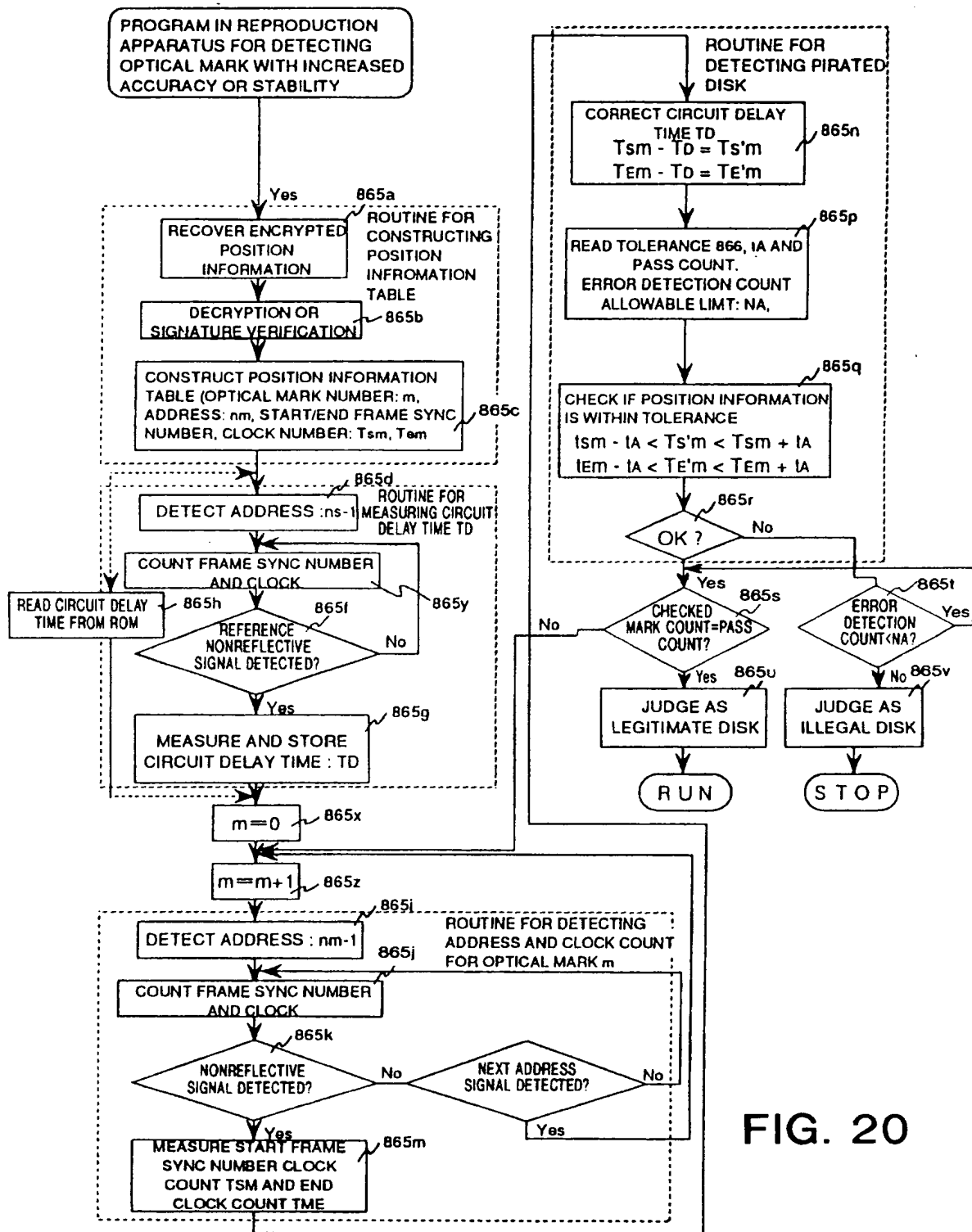


FIG. 20

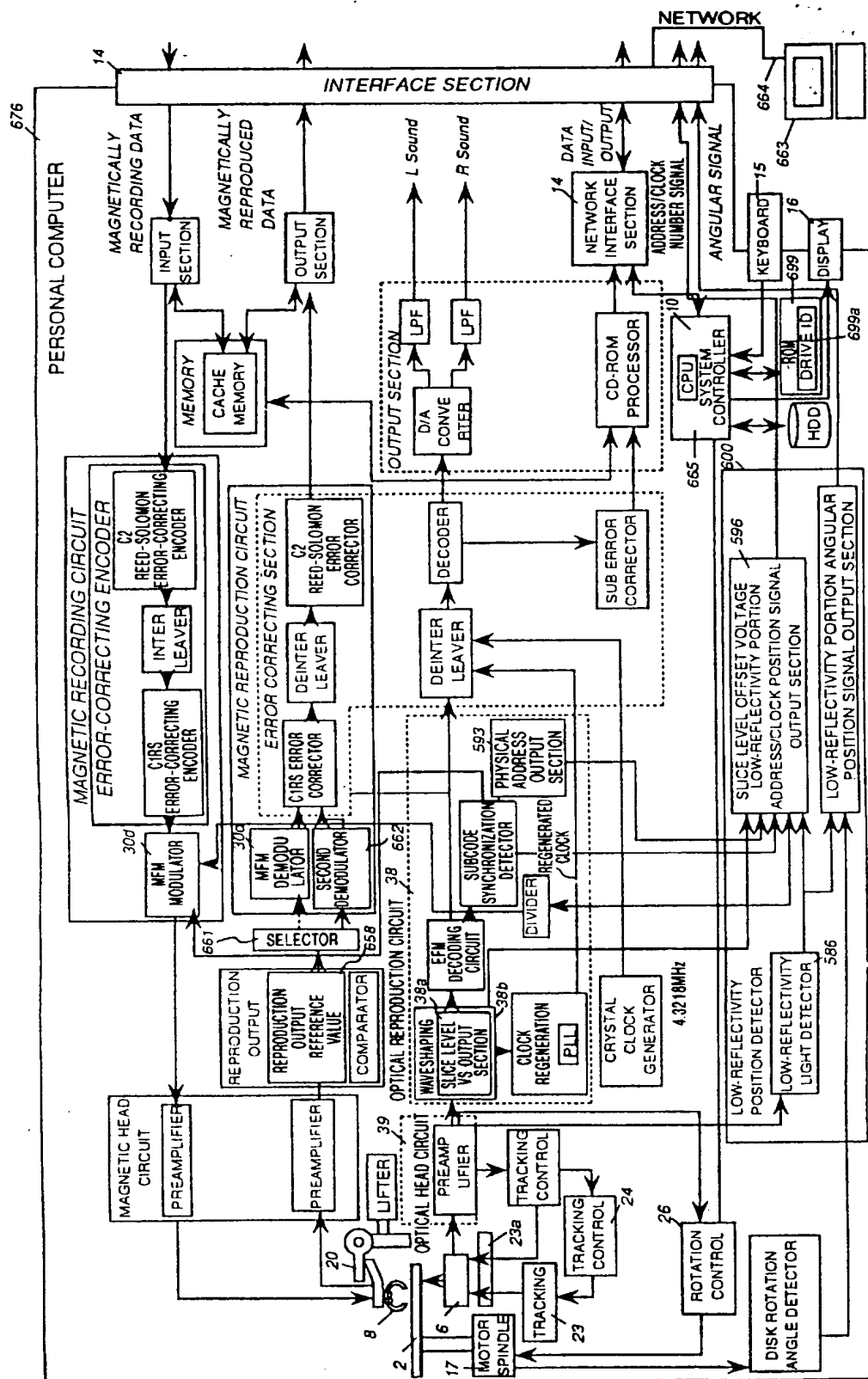
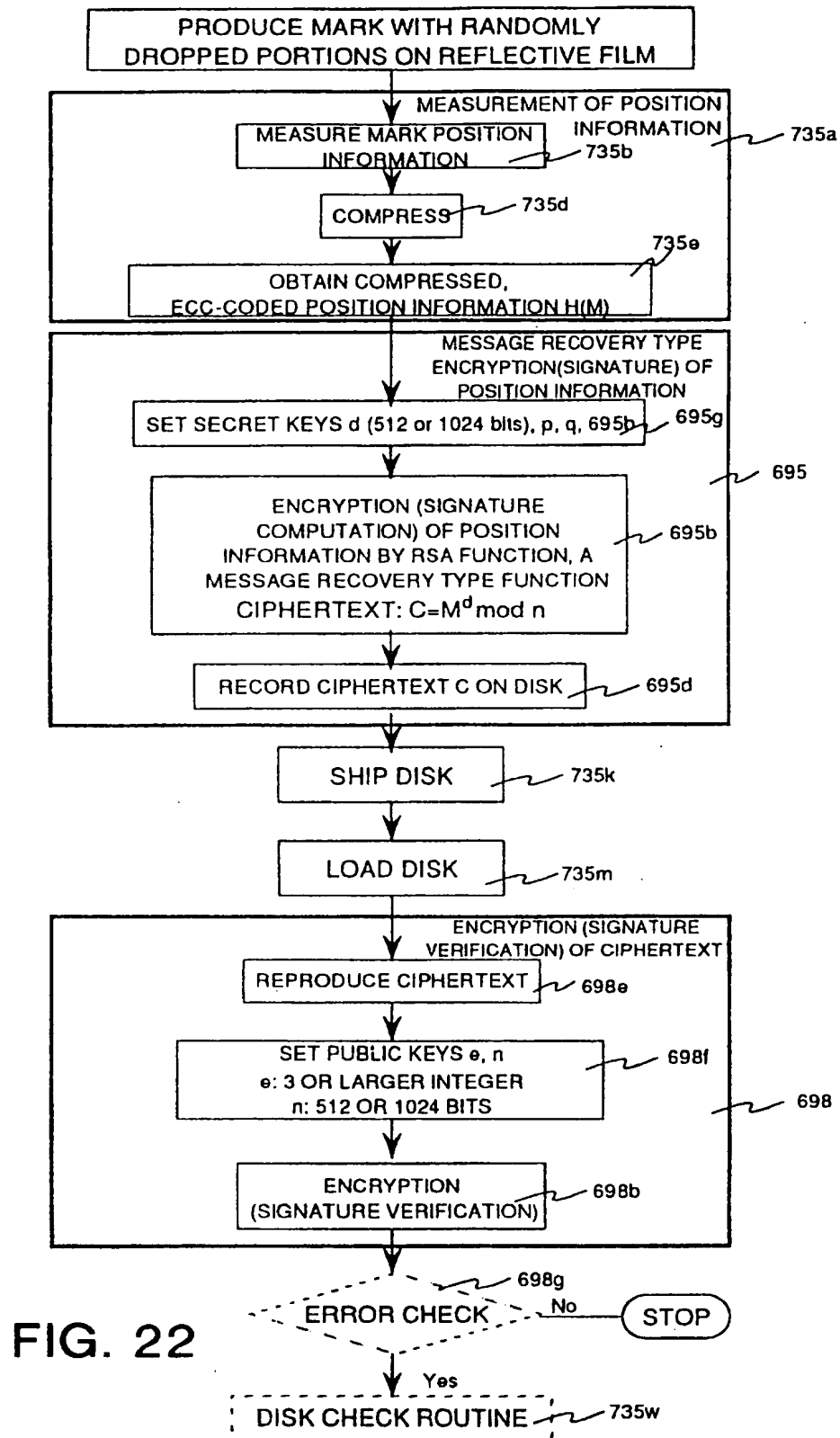


FIG. 21



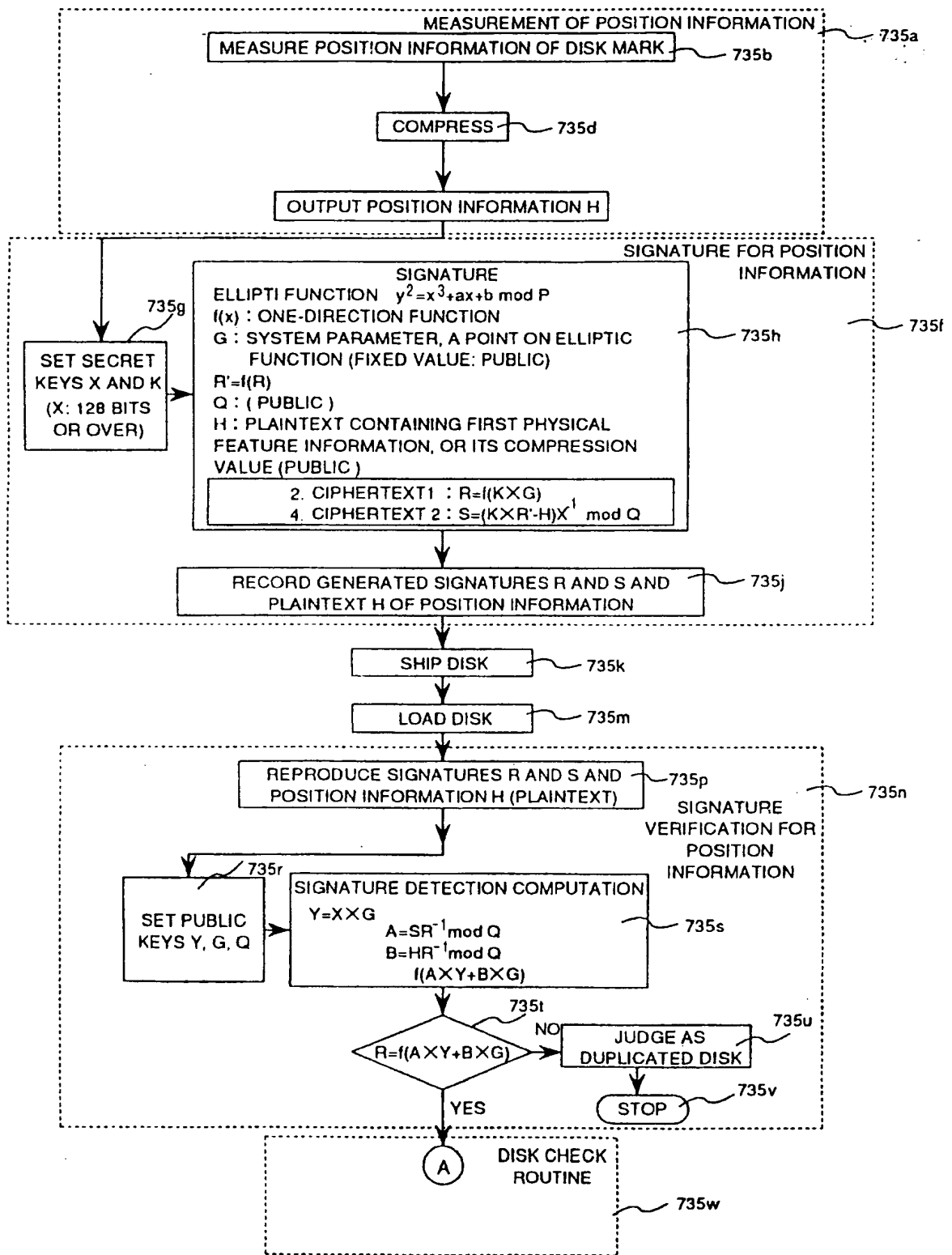


FIG. 23



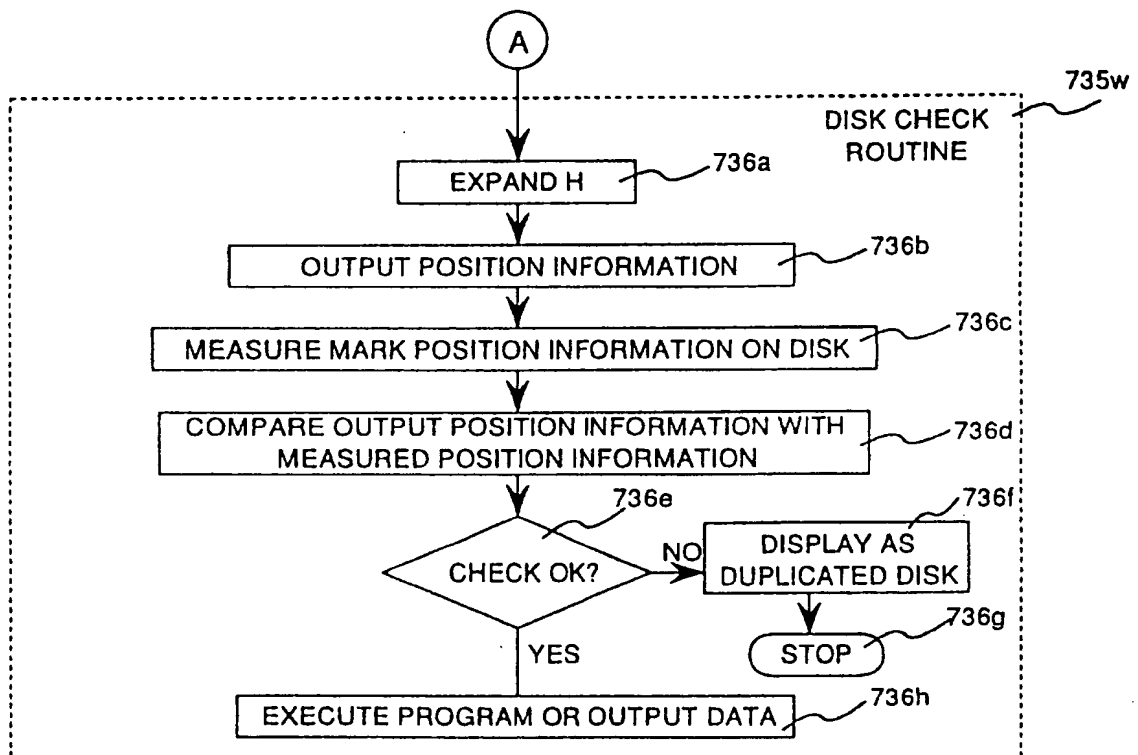


FIG. 24

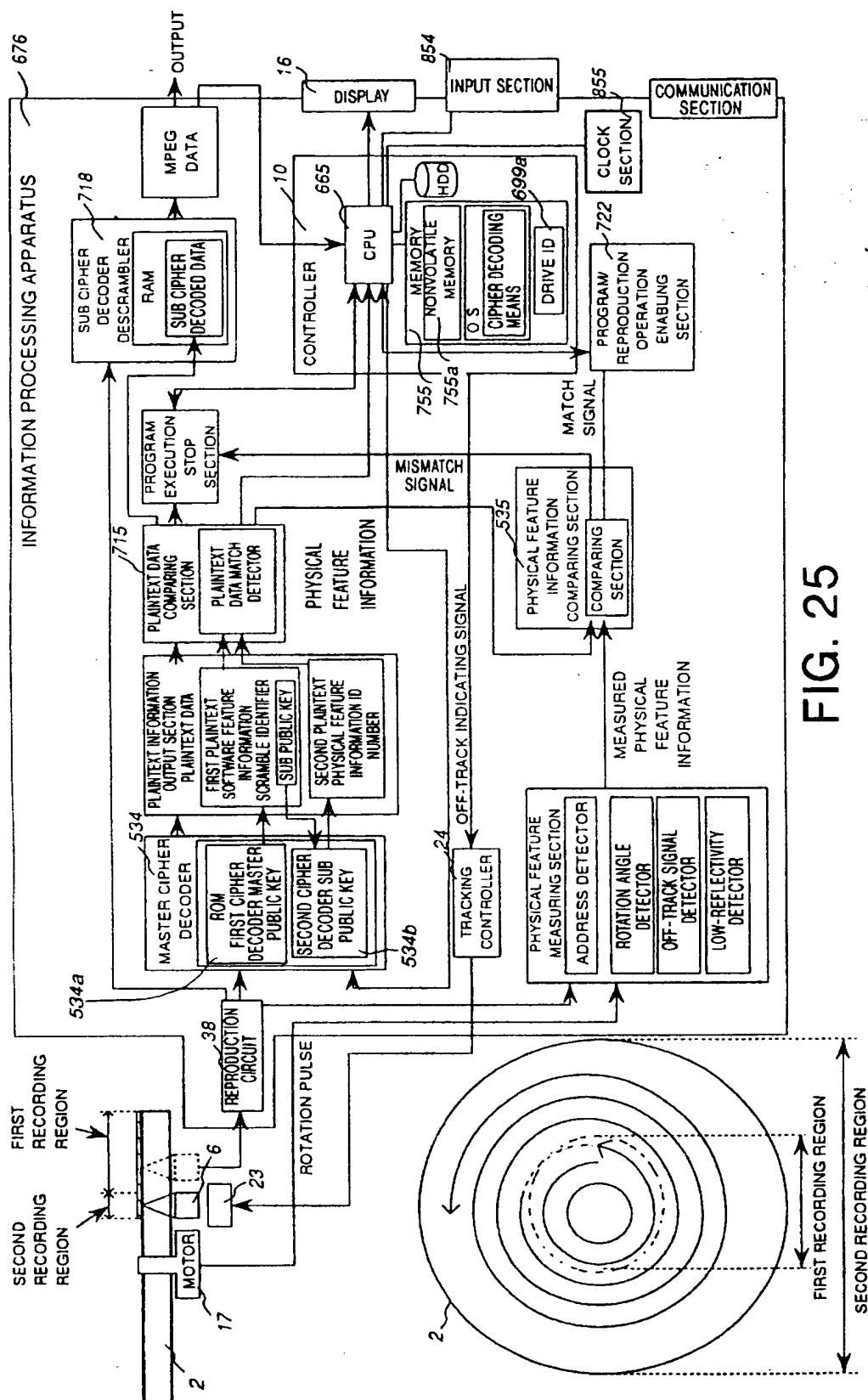
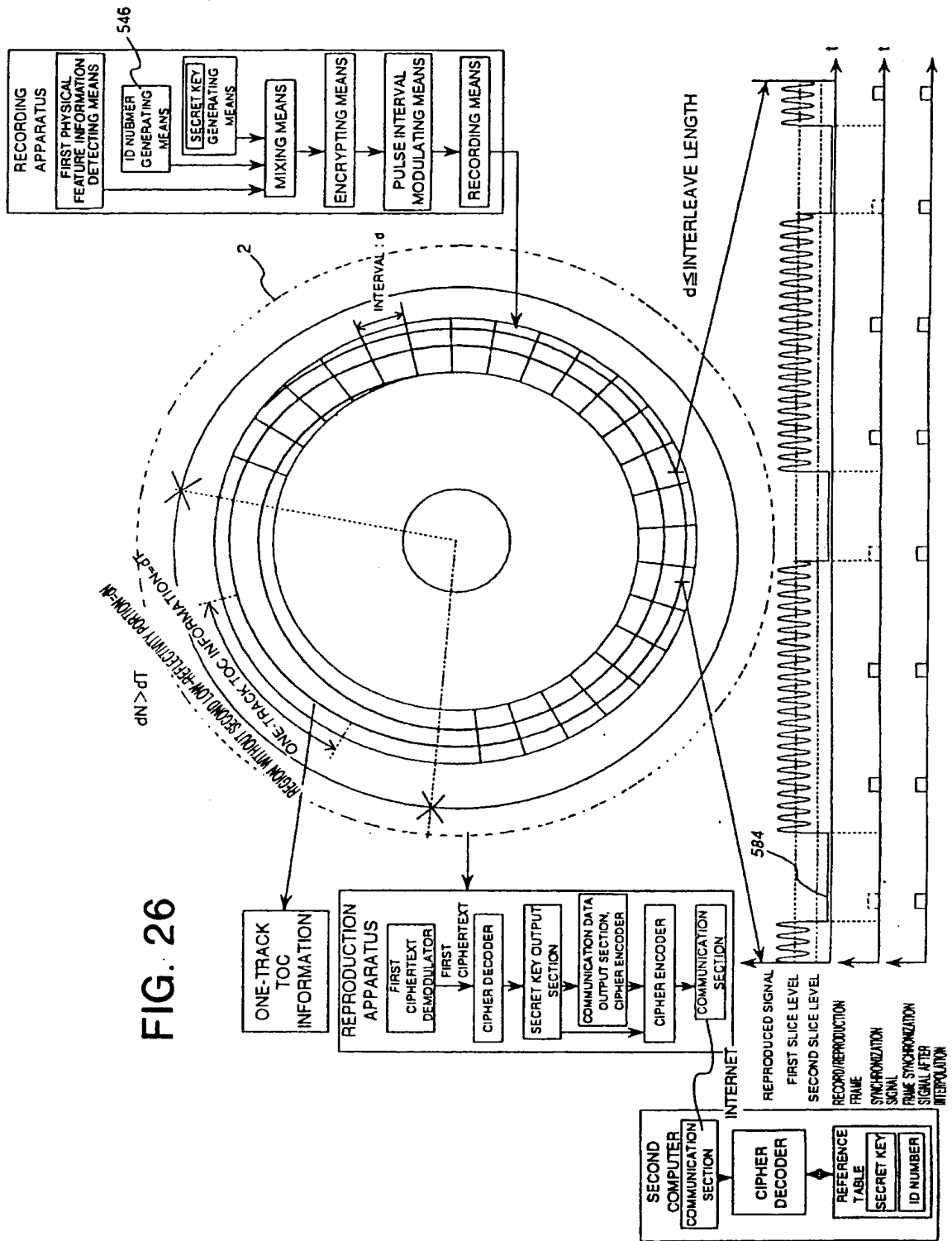
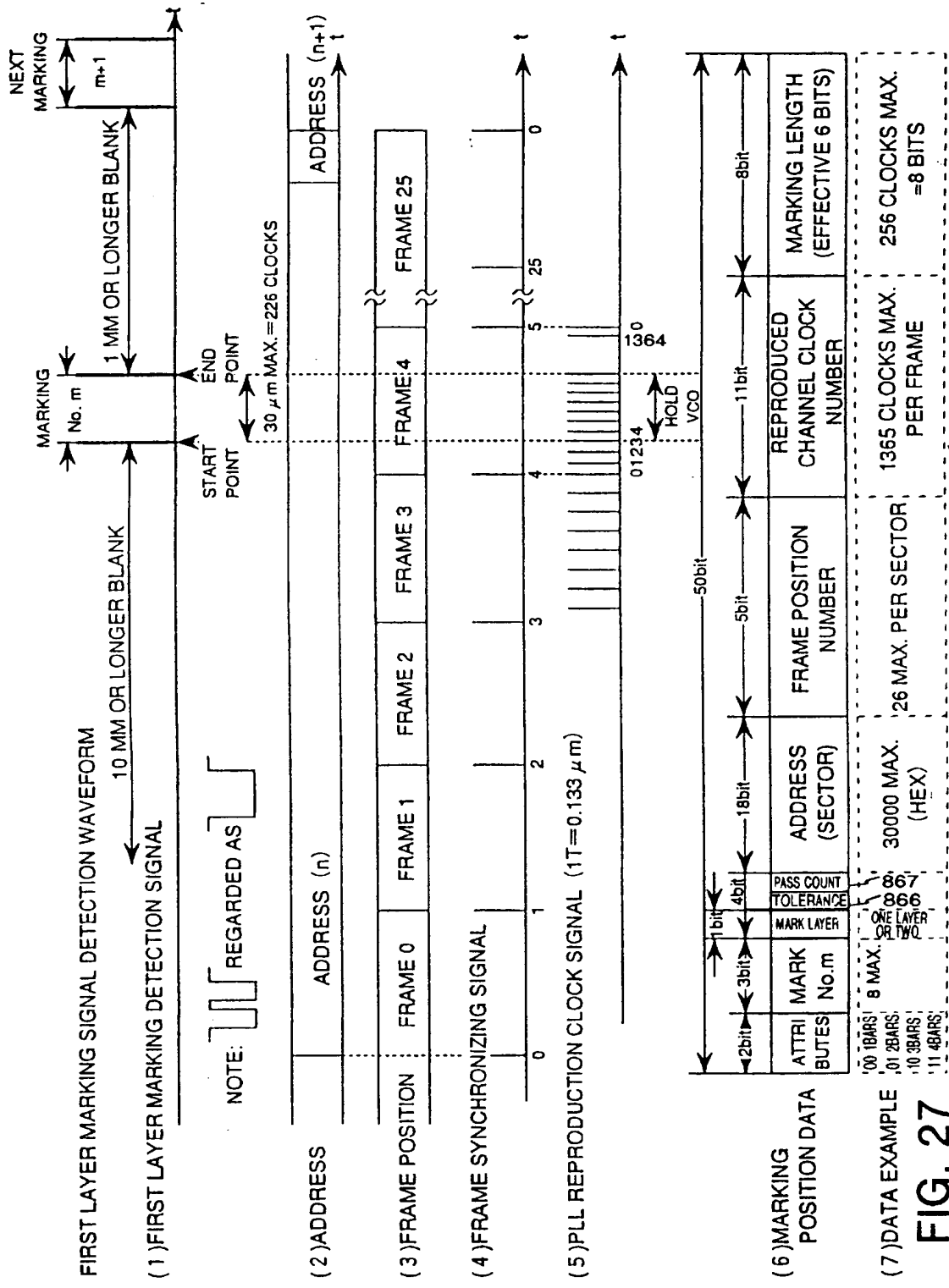
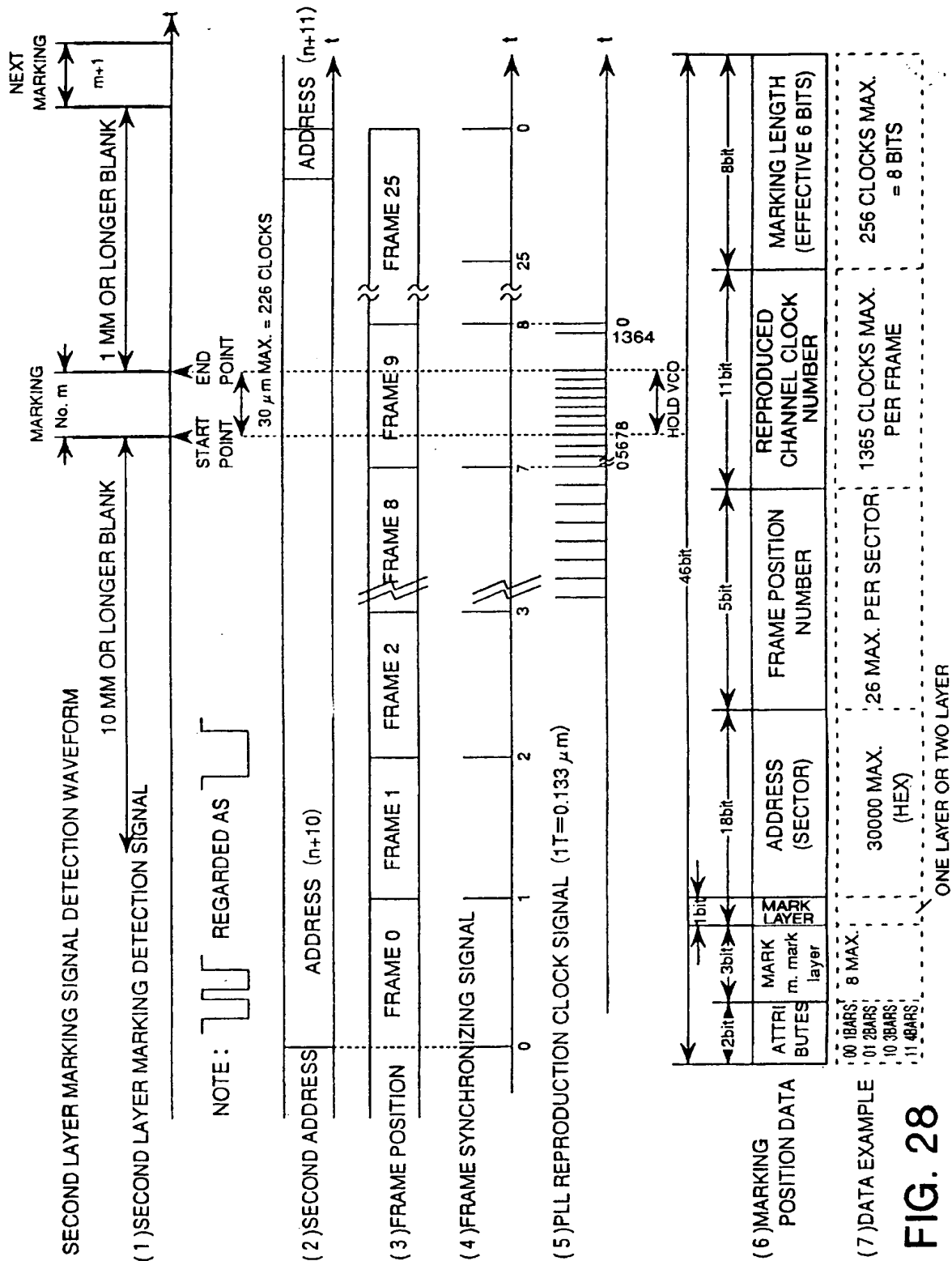


FIG. 26





**FIG. 27**



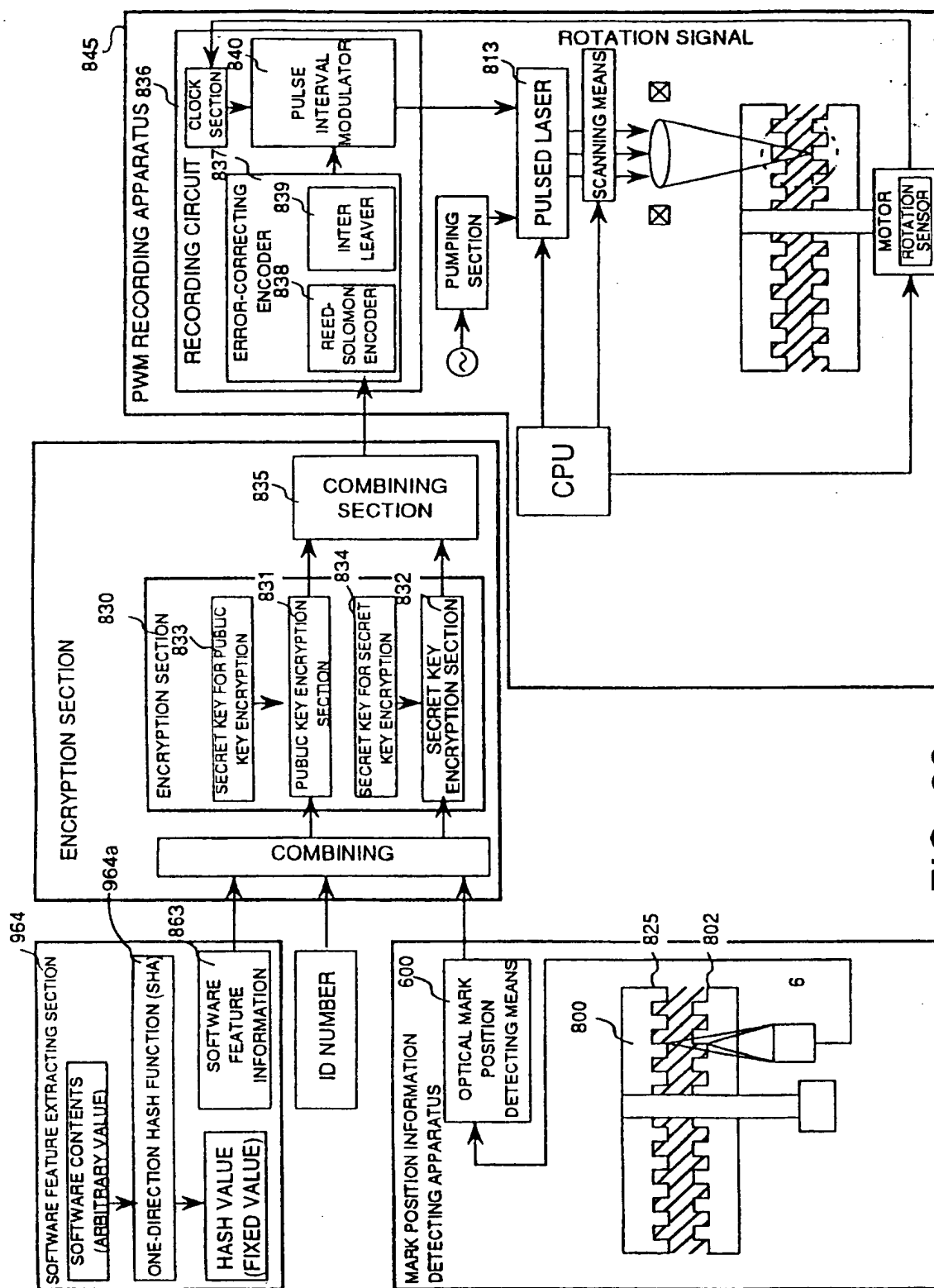
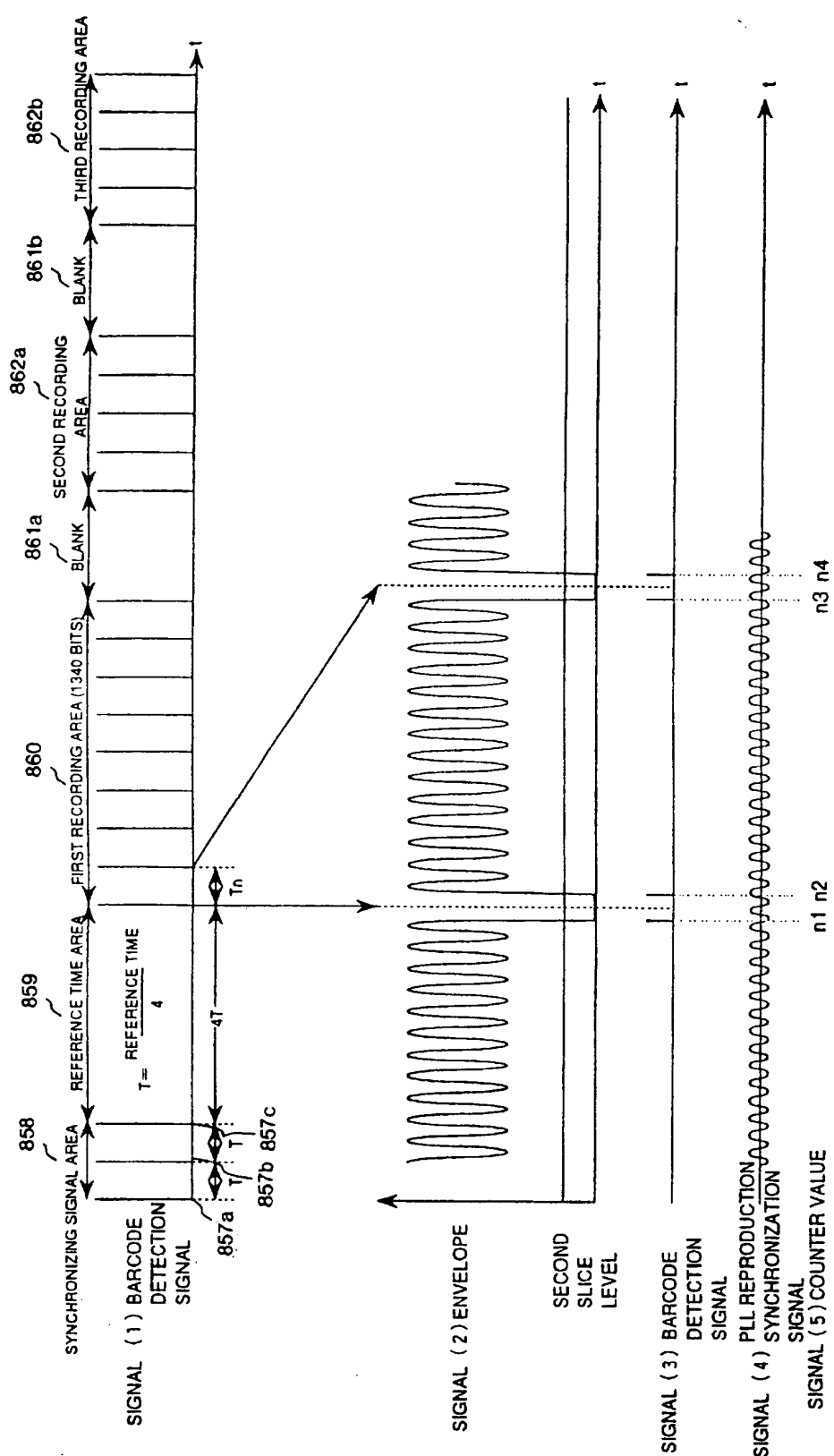


FIG. 29





$$T_n = \text{PULSE INTERVAL} = \frac{n3+n4}{2} - \frac{n1+n2}{2}$$

SIGNAL (6) DECODED VALUE  
(PROCESSED BY  
8-BIT  
MICROCOMPUTER)

1T : 00, 2T : 01, 3T : 10, 4T : 11

FIG. 31



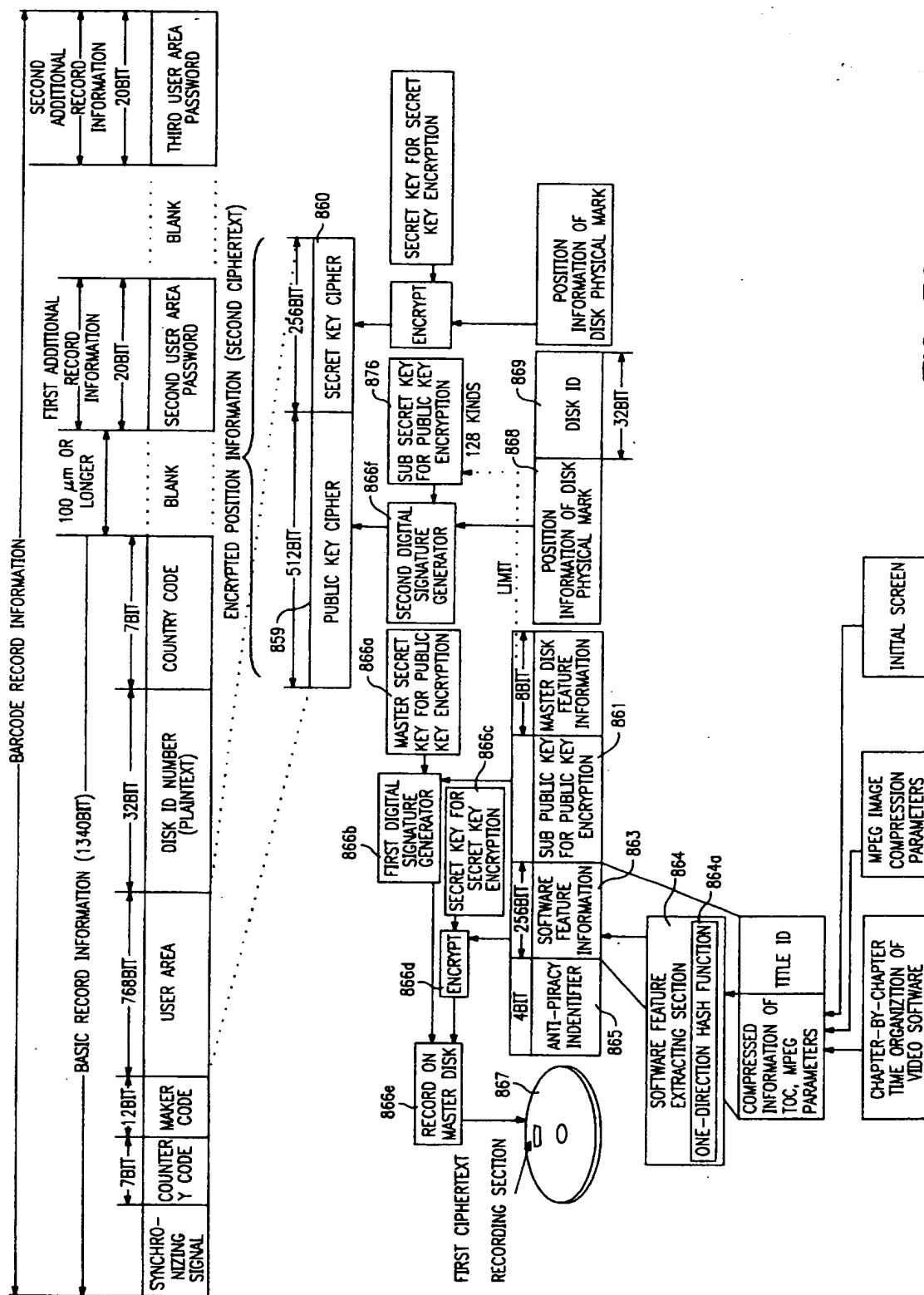


FIG. 32

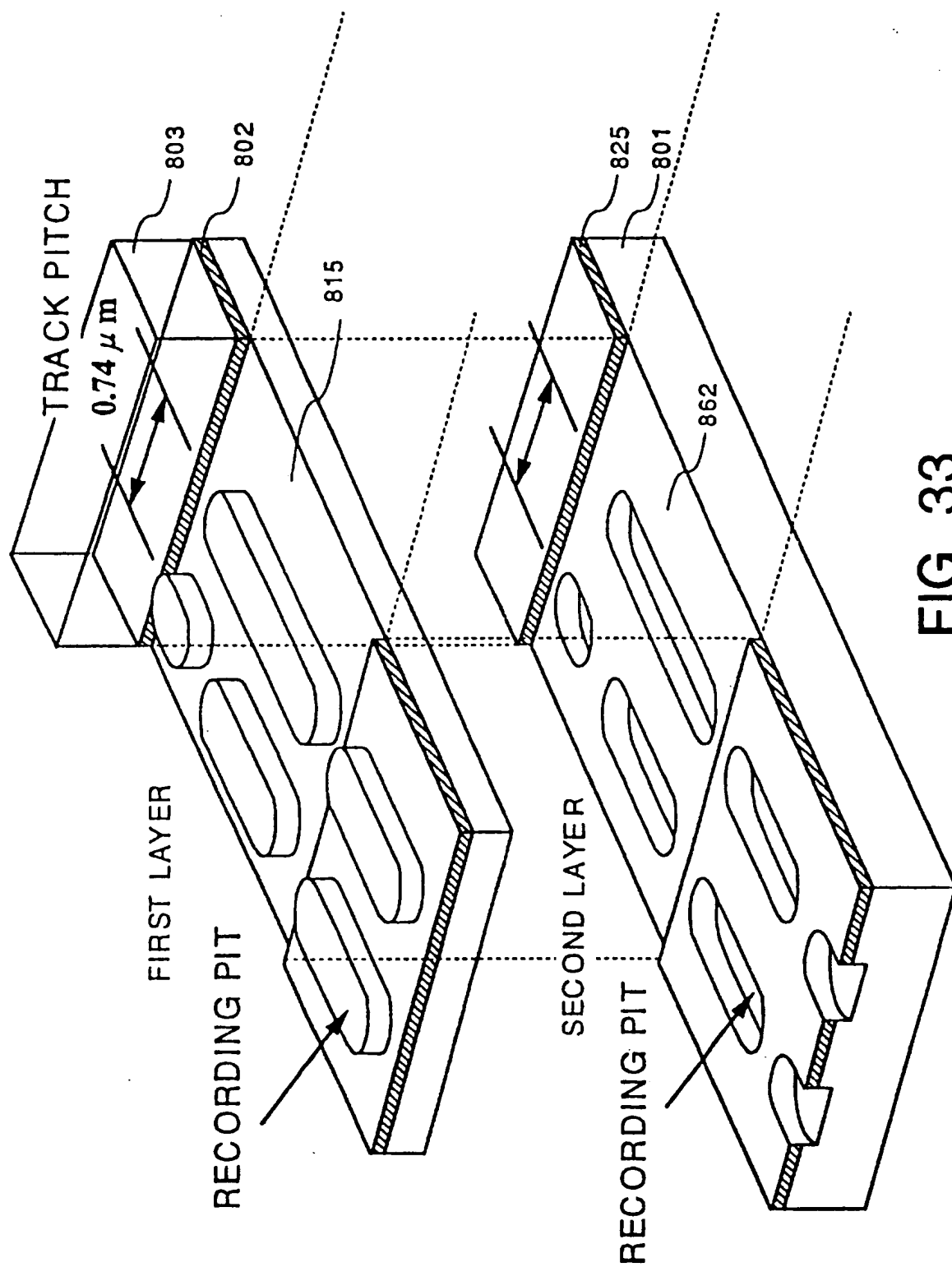
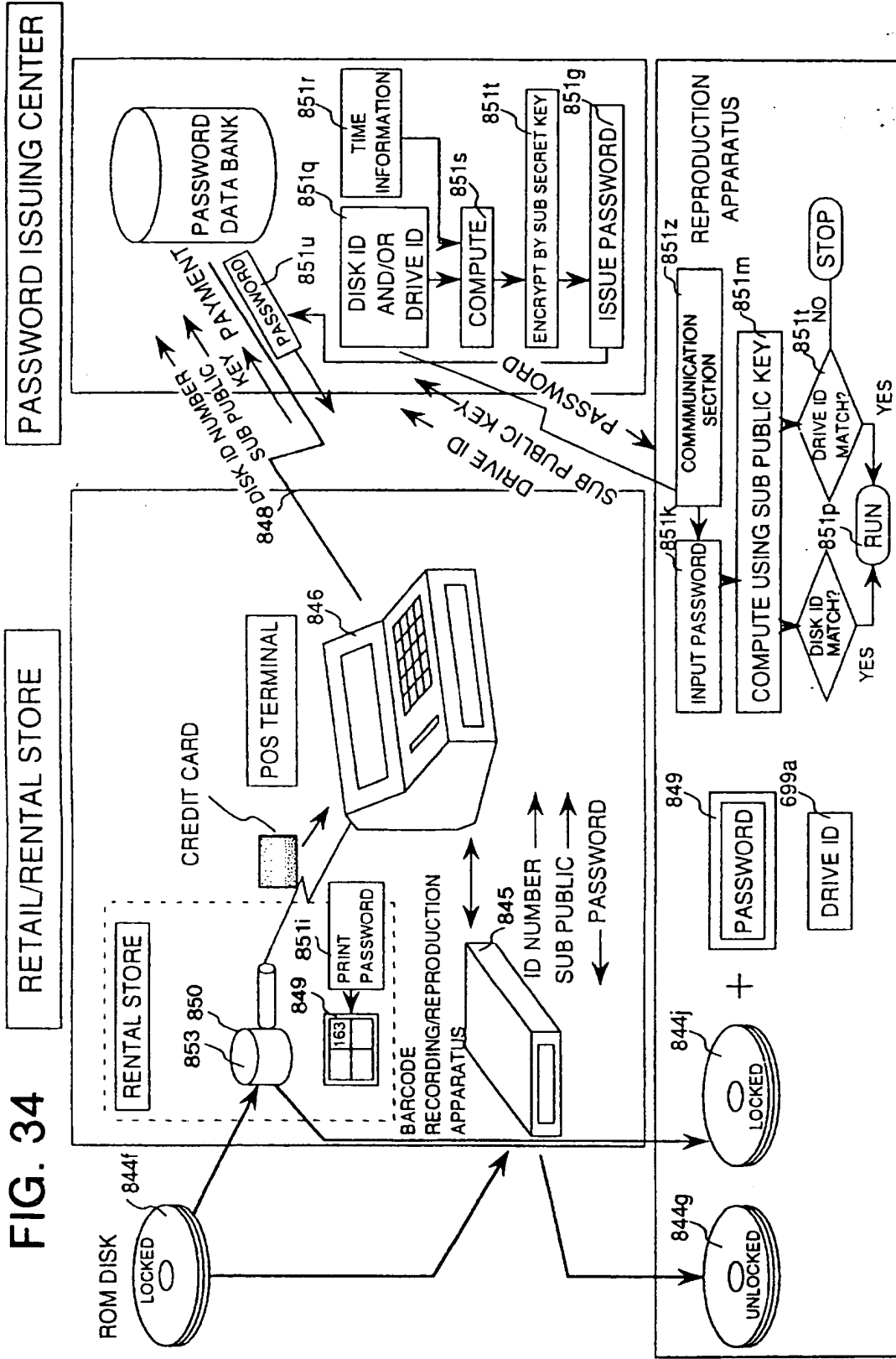


FIG. 33

FIG. 34



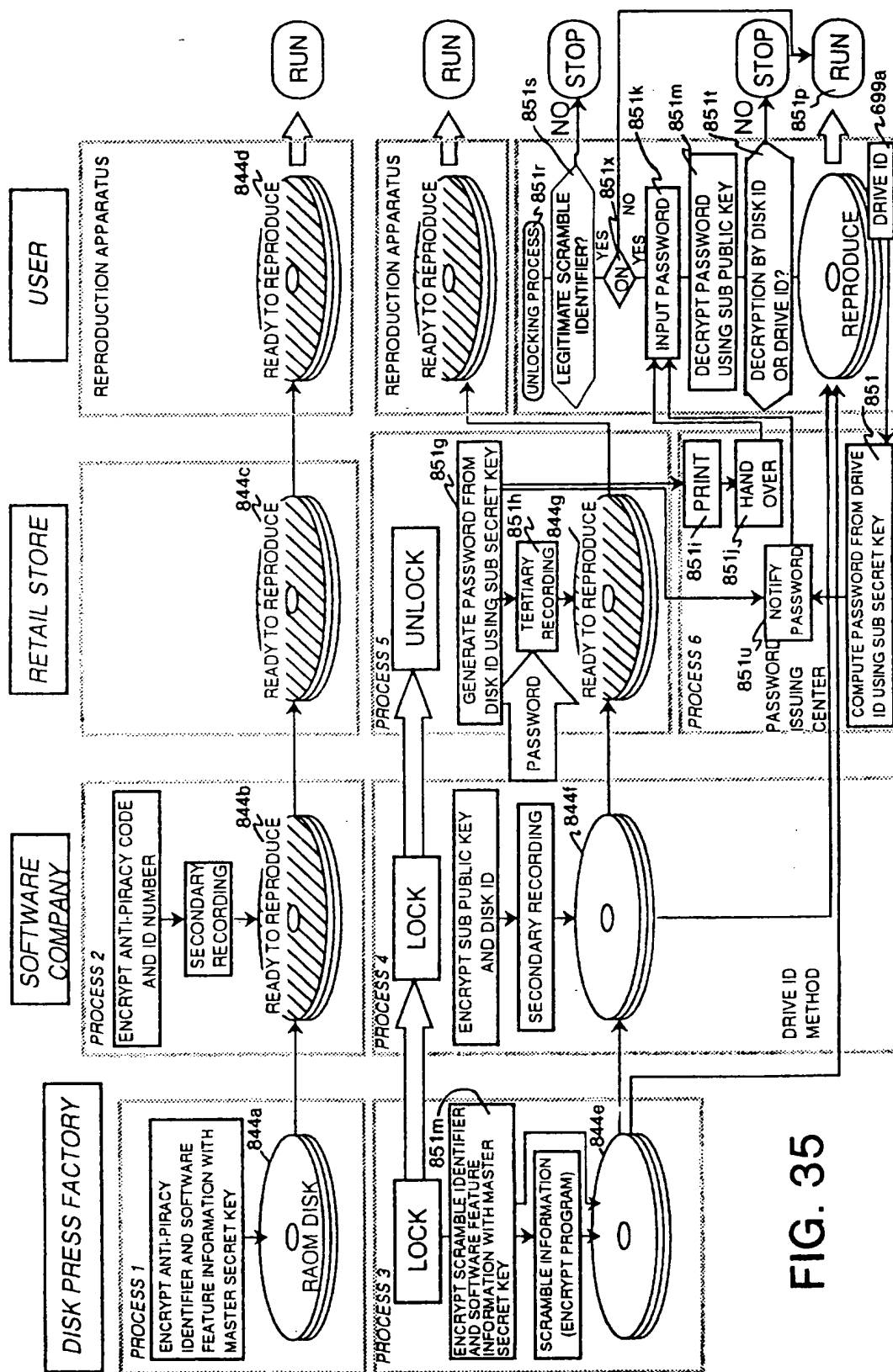


FIG. 35

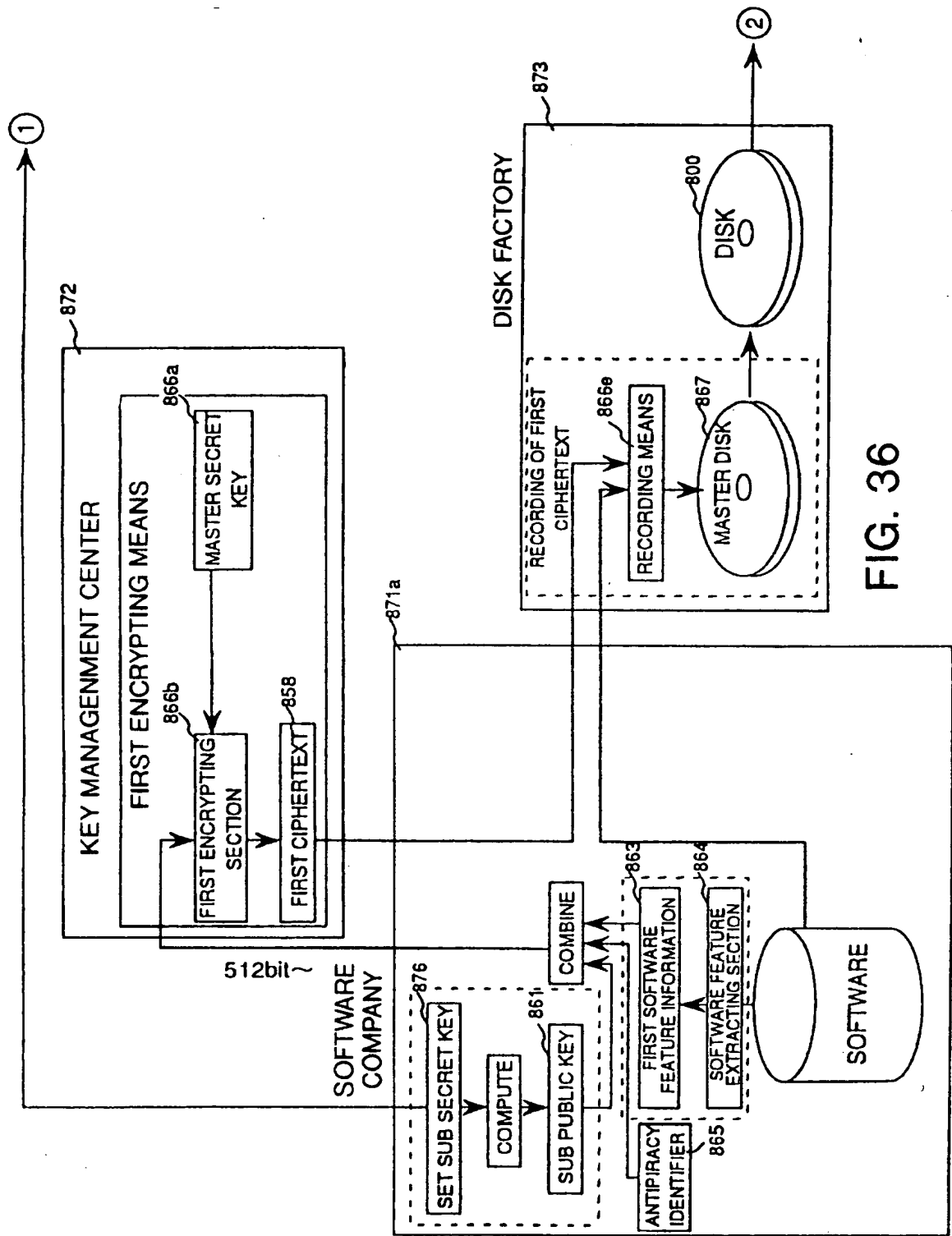


FIG. 36

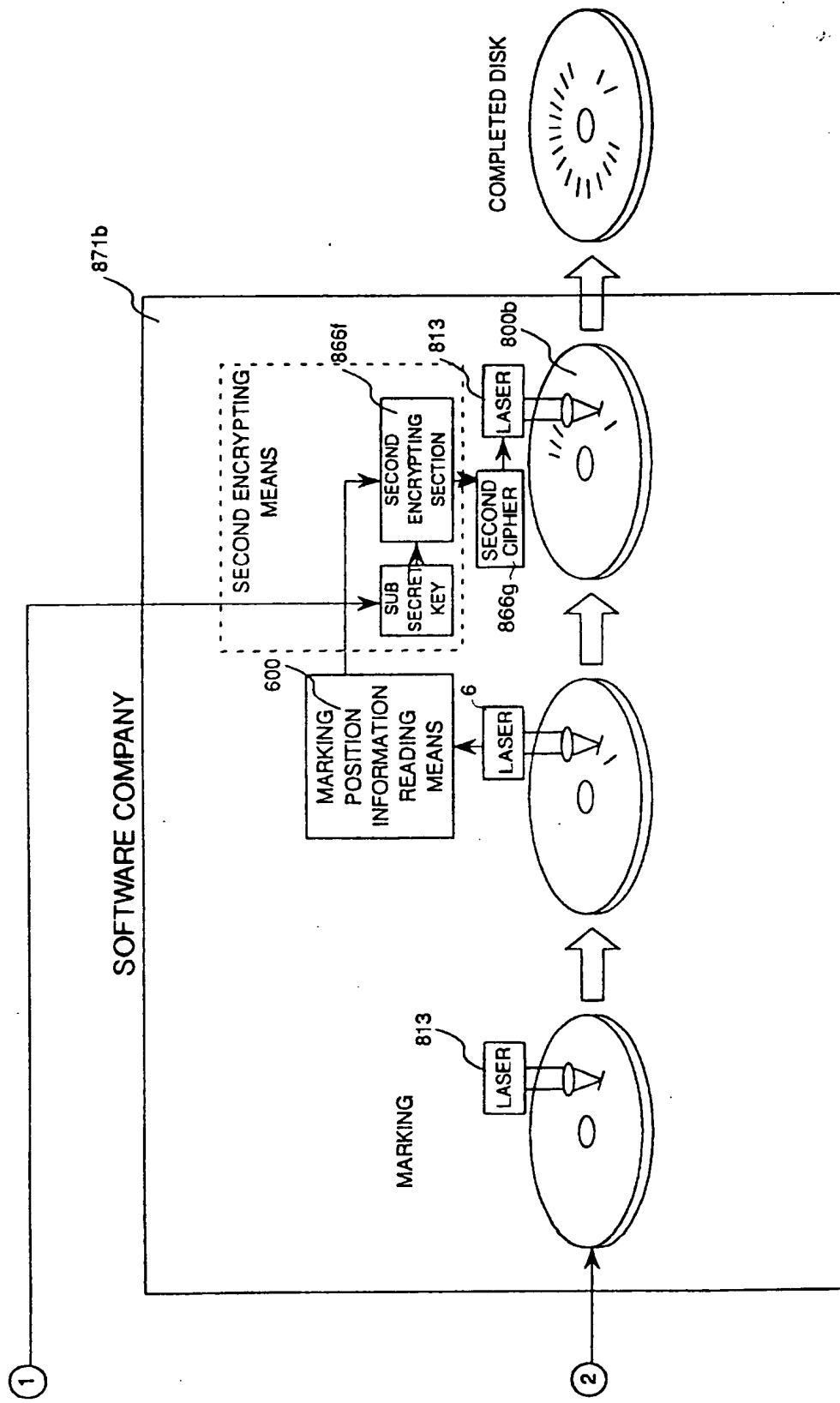


FIG. 37

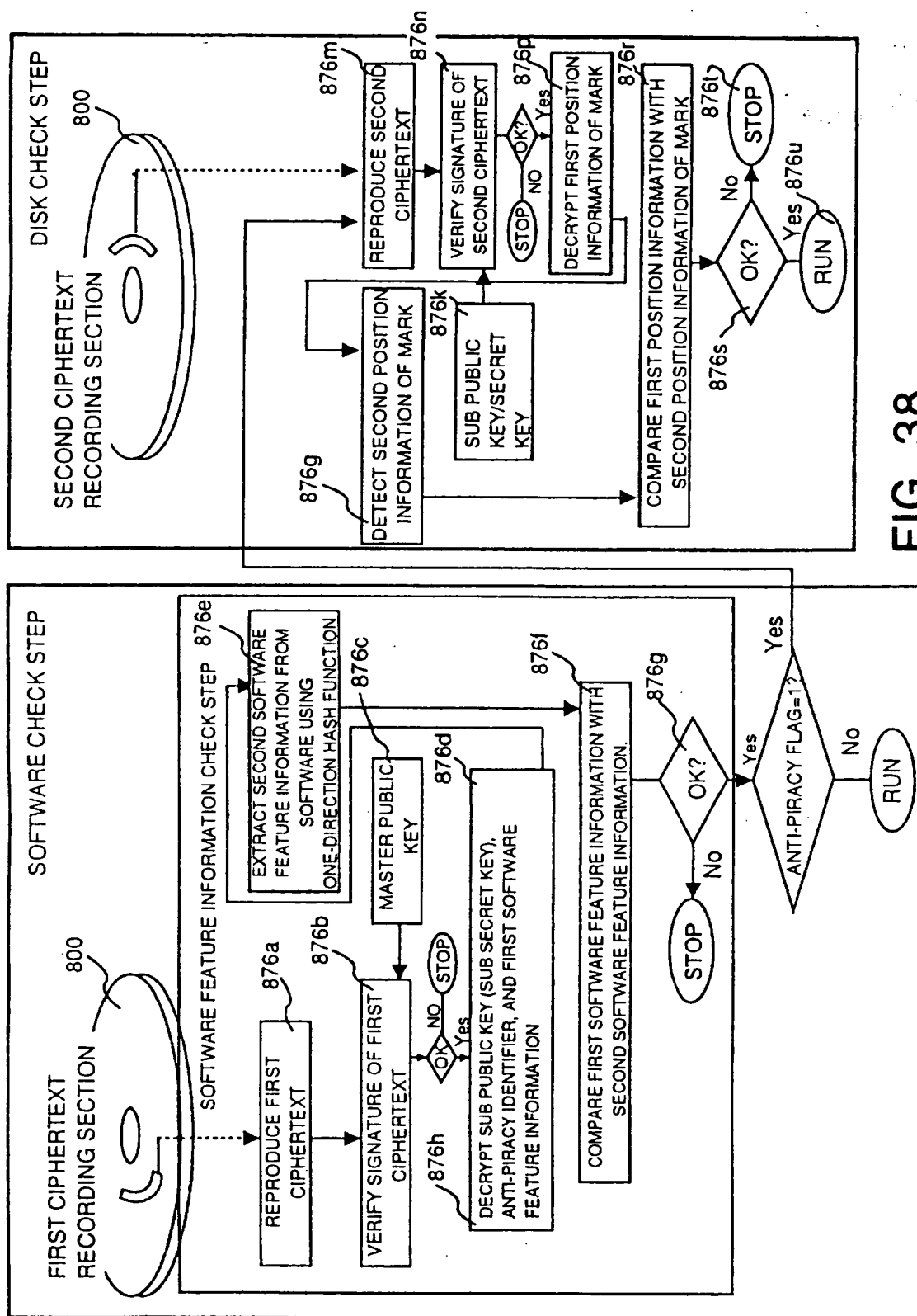
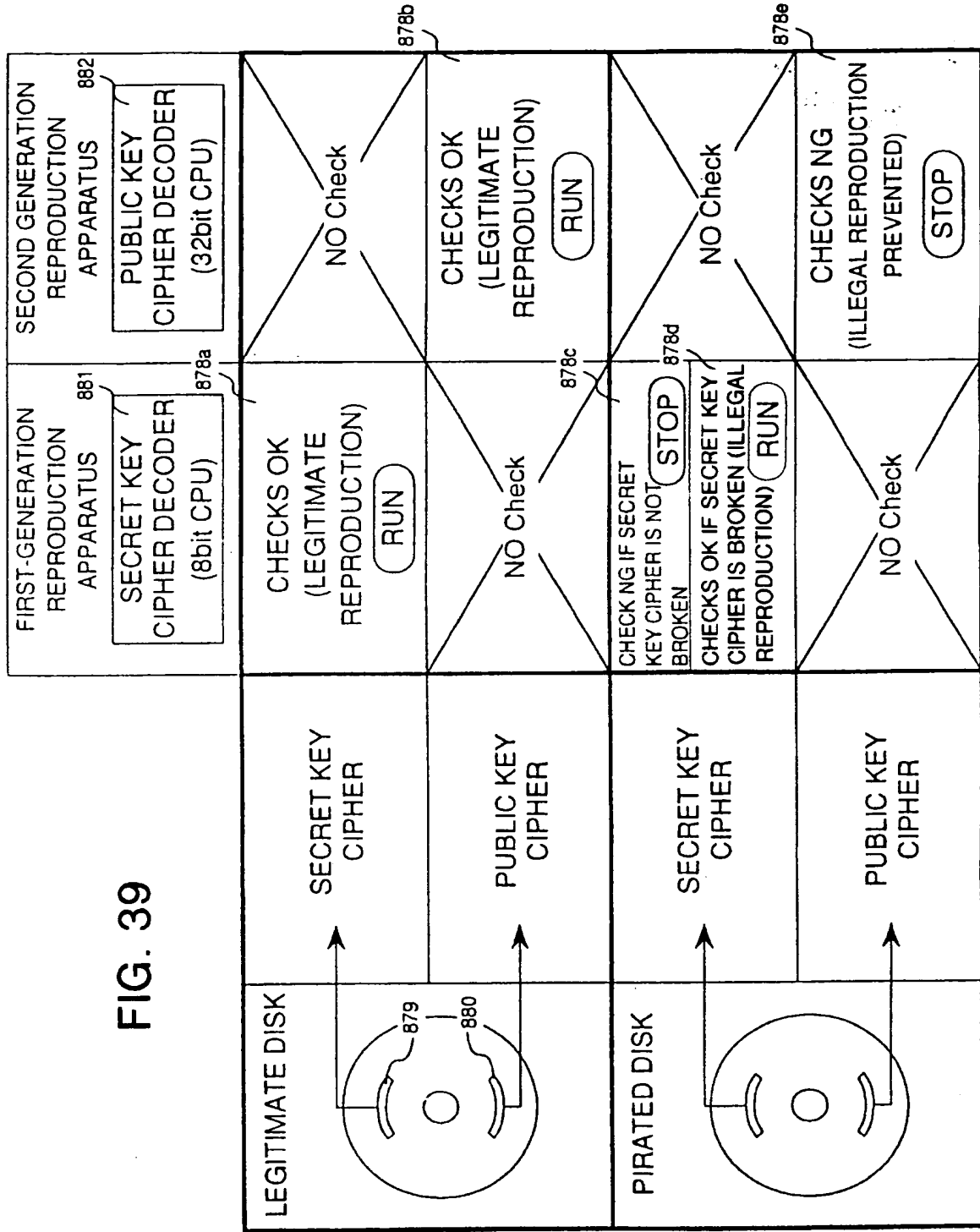


FIG. 38

FIG. 39





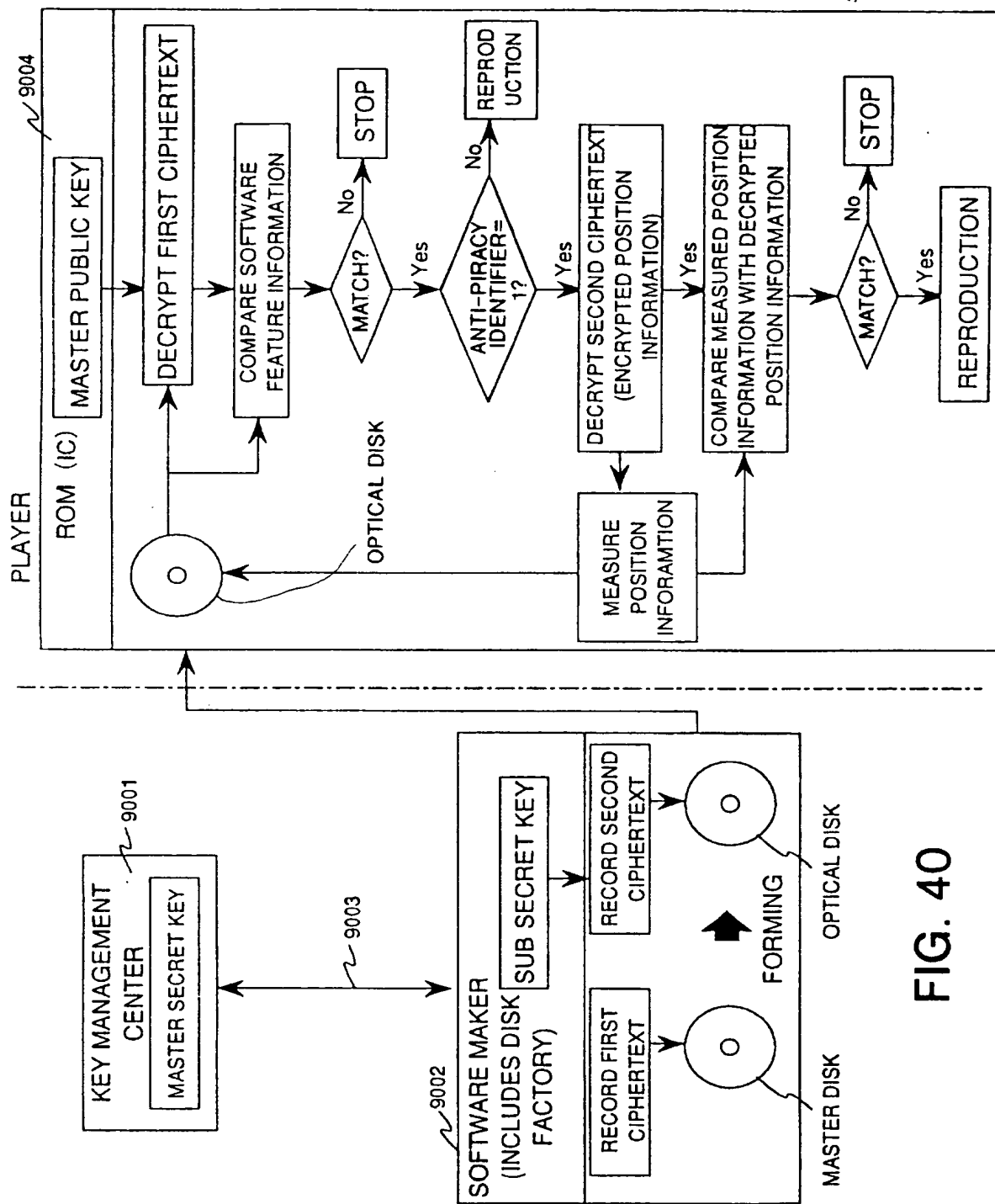


FIG. 40

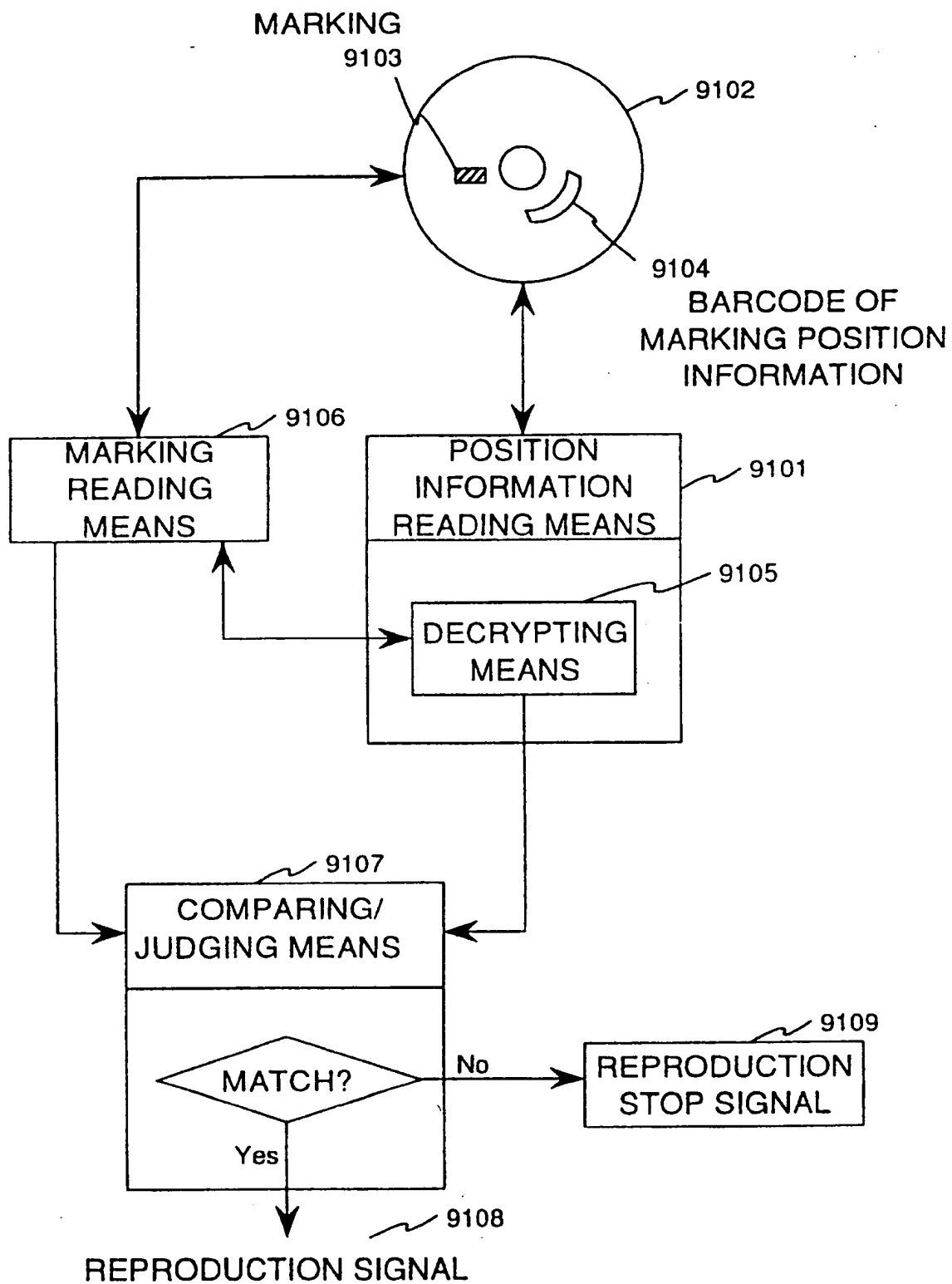


FIG. 41

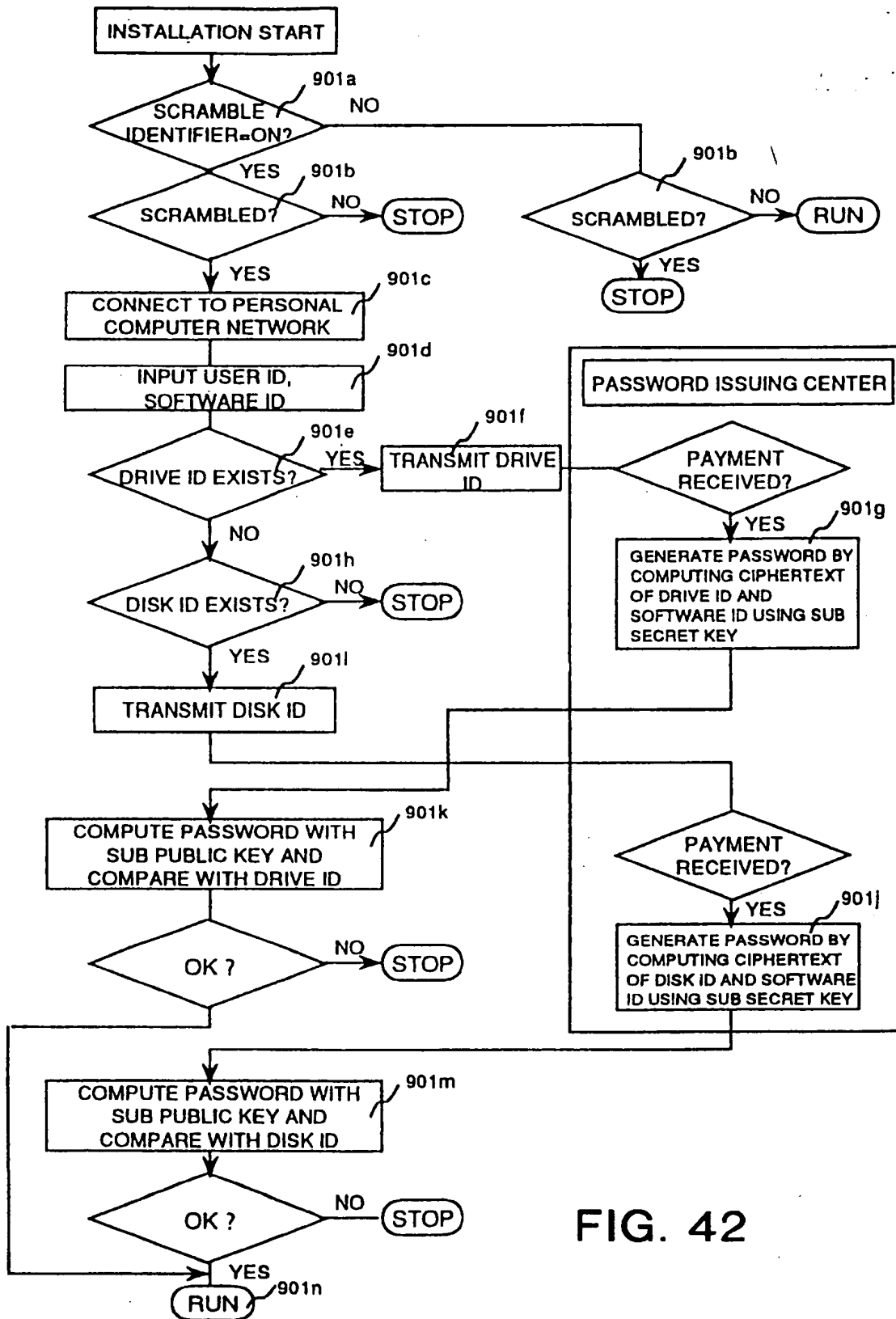


FIG. 42